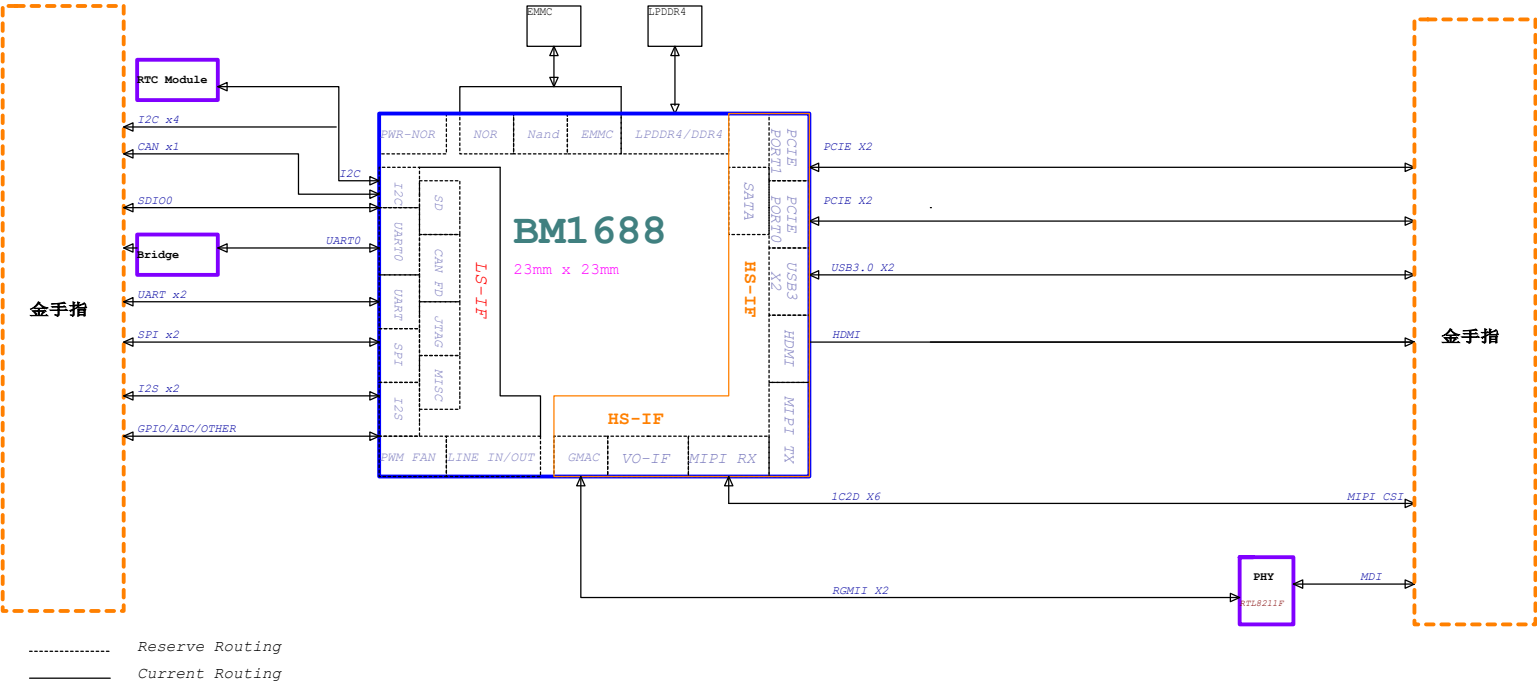
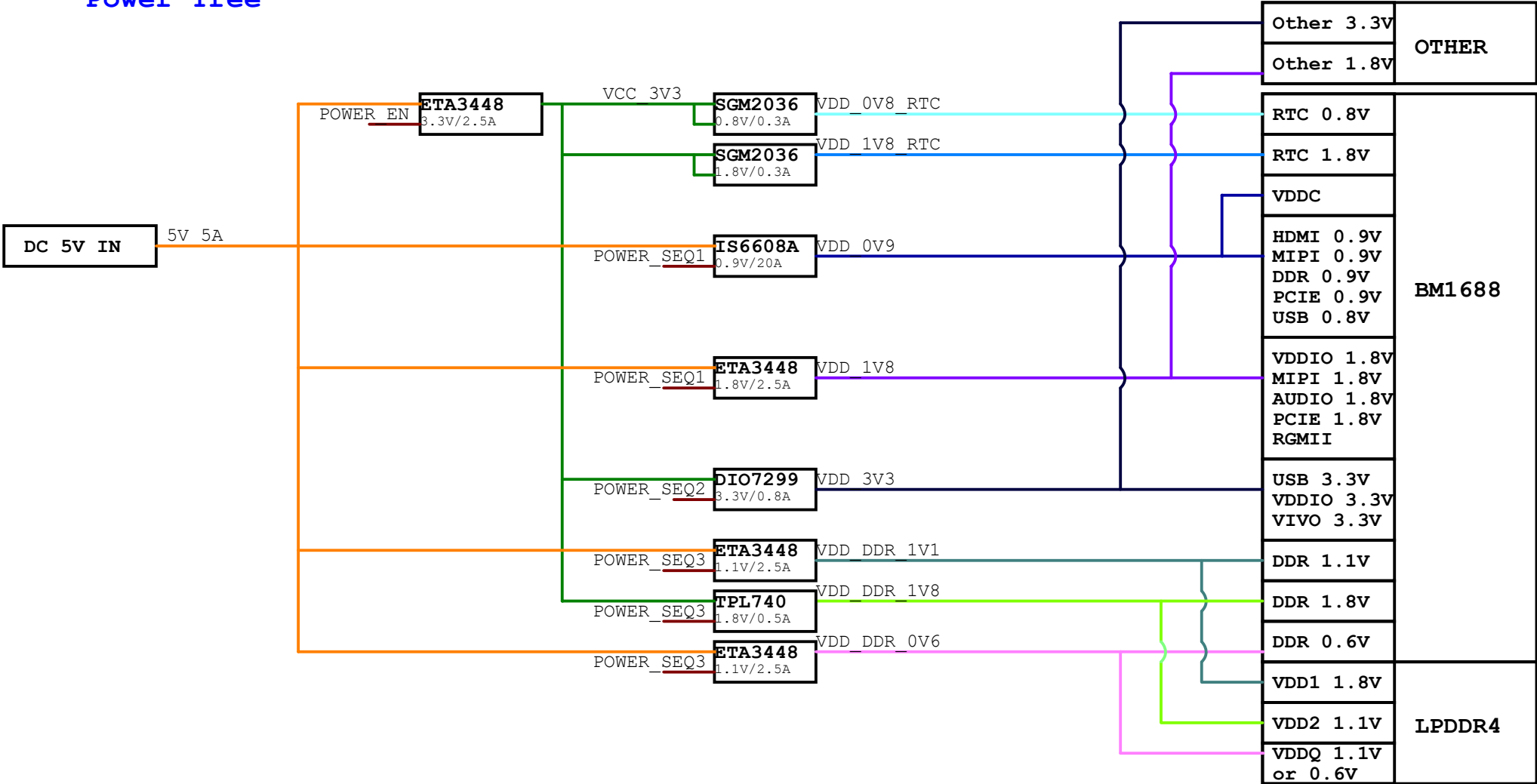


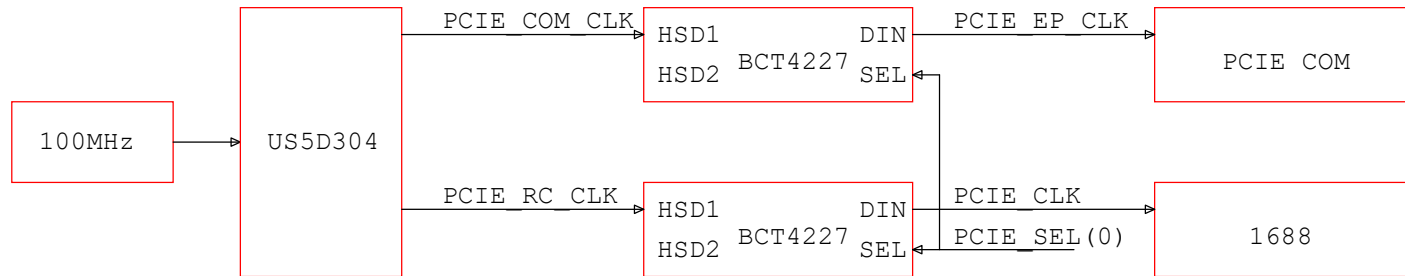
Block Diagram



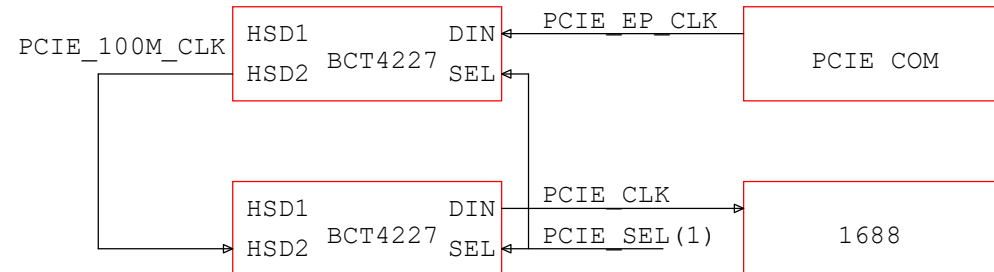
Power Tree



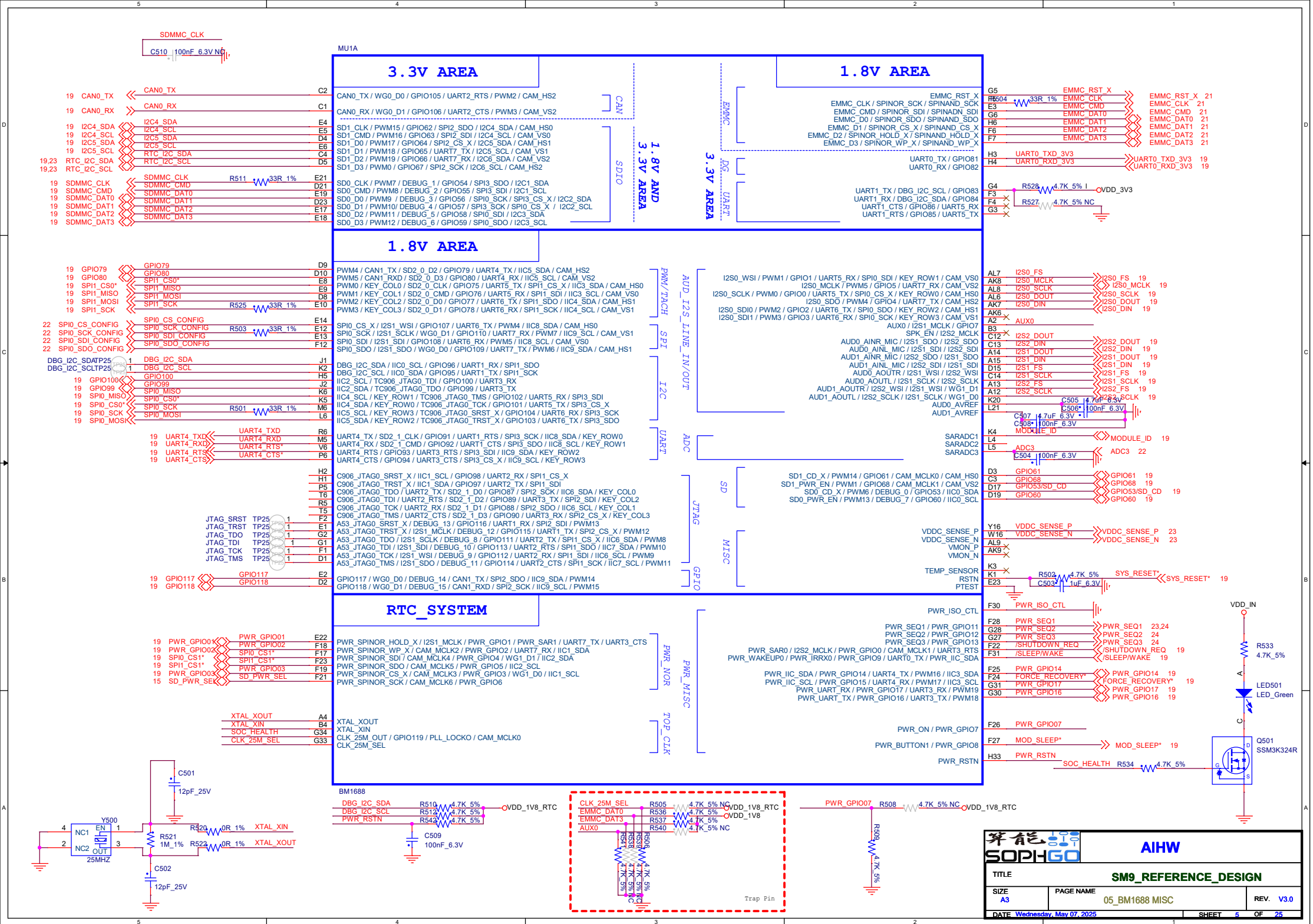
PCIE RC



PCIE EP



PCIE SET:
PCIE0:SPI0_CS
PCIE1:SPI0_SDI



MU1B

DDR CHO (LPDDR4X-DDR X16-DDR X8)

DDR0_DQ0_A / DDR0_DQ11 / DDR0_DQ11
DDR0_DQ1_A / DDR0_DQ14 / DDR0_DQ13
DDR0_DQ2_A / DDR0_DQ10 / DDR0_DQ8
DDR0_DQ3_A / DDR0_DQ8 / DDR0_DQ14
DDR0_DQ4_A / DDR0_DQ12 / DDR0_DQ12
DDR0_DQ5_A / DDR0_DM1 / DDR0_DQ10
DDR0_DQ6_A / DDR0_DQ9 / DDR0_DQ9
DDR0_DQ7_A / DDR0_DQ13 / DDR0_DM1
DDR0_DQ8_A / DDR0_DQ1 / DDR0_DM0
DDR0_DQ9_A / DDR0_DQ3 / DDR0_DQ5
DDR0_DQ10_A / DDR0_DQ5 / DDR0_DQ3
DDR0_DQ11_A / DDR0_DQ4 / DDR0_DM0
DDR0_DQ12_A / DDR0_DQ0 / DDR0_DQ2
DDR0_DQ13_A / DDR0_DQ2 / DDR0_DQ4
DDR0_DQ14_A / DDR0_DQ6 / DDR0_DQ6
DDR0_DQ15_A / DDR0_DQ7 / DDR0_DQ7
DDR0_DM0_A / DDR0_DQ15 / DDR0_DQ15
DDR0_DM1_A / DDR0_DM0 / DDR0_DQ1

DDR0_DQ0_B / DDR0_DQ17 / DDR0_DQ23
DDR0_DQ1_B / DDR0_DQ16 / DDR0_DQ16
DDR0_DQ2_B / DDR0_DQ22 / DDR0_DM2
DDR0_DQ3_B / DDR0_DQ21 / DDR0_DQ19
DDR0_DQ4_B / DDR0_DQ19 / DDR0_DQ17
DDR0_DQ5_B / DDR0_DQ23 / DDR0_DQ23
DDR0_DQ6_B / DDR0_DQ20 / DDR0_DQ22
DDR0_DQ7_B / DDR0_DQ18 / DDR0_DQ18
DDR0_DQ8_B / DDR0_DQ30 / DDR0_DQ30
DDR0_DQ9_B / DDR0_DQ24 / DDR0_DQ26
DDR0_DQ10_B / DDR0_DM3 / DDR0_DQ28
DDR0_DQ11_B / DDR0_DQ28 / DDR0_DQ24
DDR0_DQ12_B / DDR0_DQ28 / DDR0_DQ29
DDR0_DQ13_B / DDR0_DQ29 / DDR0_DM3
DDR0_DQ14_B / DDR0_DQ25 / DDR0_DQ25
DDR0_DQ15_B / DDR0_DQ27 / DDR0_DQ27
DDR0_DM0_B / DDR0_DM2 / DDR0_DQ21
DDR0_DM1_B / DDR0_DQ31 / DDR0_DQ31

DDR0_DQS1_T_A / DDR0_DQS0_P / DDR0_DQS0_P
DDR0_DQS1_C_A / DDR0_DQS0_N / DDR0_DQS0_N
DDR0_DQS0_T_A / DDR0_DQS1_P / DDR0_DQS1_P
DDR0_DQS0_C_A / DDR0_DQS1_N / DDR0_DQS1_N
DDR0_DQS0_T_B / DDR0_DQS2_P / DDR0_DQS2_P
DDR0_DQS0_C_B / DDR0_DQS2_N / DDR0_DQS2_N
DDR0_DQS1_T_B / DDR0_DQS3_P / DDR0_DQS3_P
DDR0_DQS1_C_B / DDR0_DQS3_N / DDR0_DQS3_N

DDR0_CLK_T_A / DDR0_CLK0_P / DDR0_CLK0_P
DDR0_CLK_C_A / DDR0_CLK0_N / DDR0_CLK0_N
DDR0_CLK_C_B / NC / DDR0_CLK1_N
DDR0_CLK_T_B / NC / DDR0_CLK1_P

DDR0_CA0_A / DDR0_A14 / DDR0_A3
DDR0_CA1_A / DDR0_BA0 / DDR0_A15
DDR0_CA2_A / DDR0_A0 / DDR0_A8
DDR0_CA3_A / DDR0_A15 / DDR0_A2
DDR0_CA4_A / DDR0_A13 / DDR0_A0
DDR0_CA5_A / DDR0_A6 / DDR0_A4
DDR0_CA0_B / DDR0_A11 / DDR0_A14
DDR0_CA1_B / DDR0_BG0 / DDR0_A10
DDR0_CA2_B / DDR0_ACT / DDR0_BG1
DDR0_CA3_B / DDR0_A16 / DDR0_ACT
DDR0_CA4_B / DDR0_A10 / DDR0_BG0
DDR0_CA5_B / DDR0_A8 / DDR0_BA0

DDR0_CS0_A / DDR0_A9 / DDR0_A9
DDR0_CS1_A / DDR0_A1 / DDR0_A5
DDR0_CS0_B / DDR0_CS0_N / DDR0_CS1_N
DDR0_CS1_B / DDR0_CS1_N / DDR0_CS0_N
DDR0_CKE0_A / DDR0_A7 / DDR0_A13
DDR0_CKE1_A / DDR0_BA1 / DDR0_BA1
DDR0_CKE0_B / DDR0_CKE0 / DDR0_CKE0
DDR0_CKE1_B / DDR0_A12 / DDR0_A12

NC / DDR0_A4 / DDR0_A16
NC / DDR0_A2 / DDR0_A11
NC / DDR0_A5 / DDR0_A13
NC / DDR0_A3 / DDR0_A6
NC / DDR0_ODT0 / DDR0_A1
NC / NC / DDR0_ODT0

DDR0_ZQ / DDR0_ZQ / DDR0_ZQ
DDR0_RESET_N / DDR0_RESET_N / DDR0_RESETZ

BM1688

AP28 DDR0_DQA0
AN27 DDR0_DQA1
AP26 DDR0_DQA2
AN26 DDR0_DQA3
AP25 DDR0_DQA4
AK25 DDR0_DQA5
AP29 DDR0_DQA6
AN29 DDR0_DQA7
AN32 DDR0_DQA8
AP33 DDR0_DQA9
AK29 DDR0_DQA10
AK28 DDR0_DQA11
AK26 DDR0_DQA12
AP30 DDR0_DQA13
AP31 DDR0_DQA14
AN31 DDR0_DQA15
AP27 DDR0_DMA0
AP32 DDR0_DMA1

AP15 DDR0_DQB0
AN18 DDR0_DQB1
AP17 DDR0_DQB2
AN17 DDR0_DQB3
AP18 DDR0_DQB4
AK16 DDR0_DQB5
AP14 DDR0_DQB6
AN14 DDR0_DQB7
AN11 DDR0_DQB8
AP10 DDR0_DQB9
AK12 DDR0_DQB10
AK13 DDR0_DQB11
AK15 DDR0_DQB12
AP13 DDR0_DQB13
AP12 DDR0_DQB14
AN12 DDR0_DQB15
AP16 DDR0_DMB0
AP11 DDR0_DMB1

AL29 DDR0_DQSA_T1
AL28 DDR0_DQSA_C1
AL26 DDR0_DQSA_T0
AL25 DDR0_DQSA_C0
AL15 DDR0_DQSB_T0
AL16 DDR0_DQSB_C0
AL12 DDR0_DQSB_T1
AL13 DDR0_DQSB_C1

AN24 DDR0_CLKA_T
AN23 DDR0_CLKA_C
AN20 DDR0_CLKB_C
AN19 DDR0_CLKB_T

AP24 DDR0_CAA0
AP23 DDR0_CAA1
AH24 DDR0_CAA2
AH23 DDR0_CAA3
AK22 DDR0_CAA4
AP22 DDR0_CAA5
AP19 DDR0_CAB0
AP20 DDR0_CAB1
AH18 DDR0_CAB2
AH19 DDR0_CAB3
AK19 DDR0_CAB4
AP21 DDR0_CAB5

AK23 DDR0_CSA0
AL22 DDR0_CSA1
AK18 DDR0_CSB0
AL19 DDR0_CSB1
AL23 DDR0_CKEA0
AH22 DDR0_CKEA1
AL18 DDR0_CKEB0
AH20 DDR0_CKEB1

AG20
AG24
AG23
AG22
AG18
AG15

DDR0_DQA0 17
DDR0_DQA1 17
DDR0_DQA2 17
DDR0_DQA3 17
DDR0_DQA4 17
DDR0_DQA5 17
DDR0_DQA6 17
DDR0_DQA7 17
DDR0_DQA8 17
DDR0_DQA9 17
DDR0_DQA10 17
DDR0_DQA11 17
DDR0_DQA12 17
DDR0_DQA13 17
DDR0_DQA14 17
DDR0_DQA15 17
DDR0_DMA0 17
DDR0_DMA1 17

DDR0_DQSA_T1 17
DDR0_DQSA_C1 17
DDR0_DQSA_T0 17
DDR0_DQSA_C0 17
DDR0_DQSB_T0 17
DDR0_DQSB_C0 17
DDR0_DQSB_T1 17
DDR0_DQSB_C1 17

DDR0_CLKA_T 17
DDR0_CLKA_C 17
DDR0_CLKB_C 17
DDR0_CLKB_T 17

DDR0_CAA0 17
DDR0_CAA1 17
DDR0_CAA2 17
DDR0_CAA3 17
DDR0_CAA4 17
DDR0_CAA5 17
DDR0_CAB0 17
DDR0_CAB1 17
DDR0_CAB2 17
DDR0_CAB3 17
DDR0_CAB4 17
DDR0_CAB5 17

DDR0_CSA0 17
DDR0_CSA1 17
DDR0_CSB0 17
DDR0_CSB1 17
DDR0_CKEA0 17
DDR0_CKEA1 17
DDR0_CKEB0 17
DDR0_CKEB1 17

VDD_DDR_0V6

R600 240R 1%

AK20 DDR0_RESET_N

DDR0_RESET_N 17

VDD_DDR_1V1

R244 2K 1%

DDR0_RESET_N

MU1C

DDR CH1 (LPDDR4X-DDR X16-DDR X8)

DDR1_DQ0_A / DDR1_DQ11 / DDR1_DQ11
DDR1_DQ1_A / DDR1_DQ14 / DDR1_DQ13
DDR1_DQ2_A / DDR1_DQ10 / DDR1_DQ8
DDR1_DQ3_A / DDR1_DQ8 / DDR1_DQ14
DDR1_DQ4_A / DDR1_DQ12 / DDR1_DQ12
DDR1_DQ5_A / DDR1_DM1 / DDR1_DQ10
DDR1_DQ6_A / DDR1_DQ9 / DDR1_DQ9
DDR1_DQ7_A / DDR1_DQ13 / DDR1_DM0
DDR1_DQ8_A / DDR1_DQ1 / DDR1_DM0
DDR1_DQ9_A / DDR1_DQ3 / DDR1_DQ5
DDR1_DQ10_A / DDR1_DQ5 / DDR1_DQ3
DDR1_DQ11_A / DDR1_DQ4 / DDR1_DQ0
DDR1_DQ12_A / DDR1_DQ0 / DDR1_DQ2
DDR1_DQ13_A / DDR1_DQ2 / DDR1_DQ4
DDR1_DQ14_A / DDR1_DQ6 / DDR1_DQ6
DDR1_DQ15_A / DDR1_DQ7 / DDR1_DQ7
DDR1_DM0_A / DDR1_DQ15 / DDR1_DQ15
DDR1_DM1_A / DDR1_DM0 / DDR1_DQ1

DDR1_DQ0_B / DDR1_DQ17 / DDR1_DQ23
DDR1_DQ1_B / DDR1_DQ16 / DDR1_DQ19
DDR1_DQ2_B / DDR1_DQ22 / DDR1_DM2
DDR1_DQ3_B / DDR1_DQ21 / DDR1_DQ16
DDR1_DQ4_B / DDR1_DQ19 / DDR1_DQ17
DDR1_DQ5_B / DDR1_DQ23 / DDR1_DQ23
DDR1_DQ6_B / DDR1_DQ20 / DDR1_DQ22
DDR1_DQ7_B / DDR1_DQ18 / DDR1_DQ18
DDR1_DQ8_B / DDR1_DQ30 / DDR1_DQ30
DDR1_DQ9_B / DDR1_DQ24 / DDR1_DQ26
DDR1_DQ10_B / DDR1_DM3 / DDR1_DQ28
DDR1_DQ11_B / DDR1_DQ28 / DDR1_DQ24
DDR1_DQ12_B / DDR1_DQ26 / DDR1_DQ29
DDR1_DQ13_B / DDR1_DQ29 / DDR1_DM3
DDR1_DQ14_B / DDR1_DQ25 / DDR1_DQ25
DDR1_DQ15_B / DDR1_DQ27 / DDR1_DQ27
DDR1_DM0_B / DDR1_DM2 / DDR1_DQ21
DDR1_DM1_B / DDR1_DQ31 / DDR1_DQ31

DDR1_DQS1_T_A / DDR1_DQS0_P / DDR1_DQS0_P
DDR1_DQS1_C_A / DDR1_DQS0_N / DDR1_DQS0_N
DDR1_DQS0_T_A / DDR1_DQS1_P / DDR1_DQS1_P
DDR1_DQS0_C_A / DDR1_DQS1_N / DDR1_DQS1_N
DDR1_DQS0_T_B / DDR1_DQS2_P / DDR1_DQS2_P
DDR1_DQS0_C_B / DDR1_DQS2_N / DDR1_DQS2_N
DDR1_DQS1_T_B / DDR1_DQS3_P / DDR1_DQS3_P
DDR1_DQS1_C_B / DDR1_DQS3_N / DDR1_DQS3_N

DDR1_CLK_T_A / DDR1_CLK0_P / DDR1_CLK0_P
DDR1_CLK_C_A / DDR1_CLK0_N / DDR1_CLK0_N
DDR1_CLK_T_B / NC / DDR1_CLK1_N
DDR1_CLK_C_B / NC / DDR1_CLK1_P

DDR1_CA0_A / DDR1_A14 / DDR1_A3
DDR1_CA1_A / DDR1_BA0 / DDR1_A15
DDR1_CA2_A / DDR1_A0 / DDR1_A8
DDR1_CA3_A / DDR1_A15 / DDR1_A2
DDR1_CA4_A / DDR1_A13 / DDR1_A0
DDR1_CA5_A / DDR1_A6 / DDR1_A4
DDR1_CA0_B / DDR1_A11 / DDR1_A14
DDR1_CA1_B / DDR1_BG0 / DDR1_A10
DDR1_CA2_B / DDR1_ACT / DDR1_BG1
DDR1_CA3_B / DDR1_A16 / DDR1_ACT
DDR1_CA4_B / DDR1_A10 / DDR1_BG0
DDR1_CA5_B / DDR1_A8 / DDR1_BA0

DDR1_CS0_A / DDR1_A9 / DDR1_A9
DDR1_CS1_A / DDR1_A1 / DDR1_A5
DDR1_CS0_B / DDR1_CS0_N / DDR1_CS1_N
DDR1_CS1_B / DDR1_CS1_N / DDR1_CS0_N
DDR1_CKE0_A / DDR1_A7 / DDR1_A13
DDR1_CKE1_A / DDR1_BA1 / DDR1_BA1
DDR1_CKE0_B / DDR1_CKE0 / DDR1_CKE0
DDR1_CKE1_B / DDR1_A12 / DDR1_A12

NC / DDR1_A4 / DDR1_A16
NC / DDR1_A2 / DDR1_A11
NC / DDR1_A5 / DDR1_A13
NC / DDR1_A3 / DDR1_A6
NC / DDR1_ODT0 / DDR1_A1
NC / NC / DDR1_ODT0

DDR1_ZQ / DDR1_ZQ / DDR1_ZQ
DDR1_RESET_N / DDR1_RESET_N / DDR1_RESETZ

R34 DDR1_DQA0
T33 DDR1_DQA1
U34 DDR1_DQA2
U33 DDR1_DQA3
V34 DDR1_DQA4
T30 DDR1_DQA5
P34 DDR1_DQA6
P33 DDR1_DQA7
L33 DDR1_DQA8
K34 DDR1_DQA9
M30 DDR1_DQA10
N30 DDR1_DQA11
R30 DDR1_DQA12
N34 DDR1_DQA13
M34 DDR1_DQA14
M33 DDR1_DQA15
T34 DDR1_DMA0
L34 DDR1_DMA1

AH34 DDR1_DQB0
AG33 DDR1_DQB1
AF34 DDR1_DQB2
AF33 DDR1_DQB3
AE34 DDR1_DQB4
AE30 DDR1_DQB5
AJ34 DDR1_DQB6
AJ33 DDR1_DQB7
AK33 DDR1_DQB8
AN34 DDR1_DQB9
AJ30 DDR1_DQB10
AH30 DDR1_DQB11
AF30 DDR1_DQB12
AK34 DDR1_DQB13
AL34 DDR1_DQB14
AL33 DDR1_DQB15
AG34 DDR1_DMB0
AM34 DDR1_DMB1

M31 DDR1_DQSA_T1
N31 DDR1_DQSA_C1
R31 DDR1_DQSA_T0
T31 DDR1_DQSA_C0
AF31 DDR1_DQSB_T0
AE31 DDR1_DQSB_C0
AJ31 DDR1_DQSB_T1
AH31 DDR1_DQSB_C1

W33 DDR1_CLKA_T
Y33 DDR1_CLKA_C
AD33 DDR1_CLKB_T
AC33 DDR1_CLKB_C

W34 DDR1_CAA0
Y34 DDR1_CAA1
V28 DDR1_CAA2
W28 DDR1_CAA3
W30 DDR1_CAA4
AA34 DDR1_CAA5
AD34 DDR1_CAB0
AC34 DDR1_CAB1
AD28 DDR1_CAB2
AC28 DDR1_CAB3
AB30 DDR1_CAB4
AB34 DDR1_CAB5

V30 DDR1_CSA0
W31 DDR1_CSA1
AC30 DDR1_CSB0
AB31 DDR1_CSB1
V31 DDR1_CKEA0
Y28 DDR1_CKEA1
AC31 DDR1_CKEB0
AB28 DDR1_CKEB1

AB27
V27
W27
Y27
AD27
AC27

AA31 R600 240R 1%
AA30 DDR1_RESET_N

DDR1_DQA0 18
DDR1_DQA1 18
DDR1_DQA2 18
DDR1_DQA3 18
DDR1_DQA4 18
DDR1_DQA5 18
DDR1_DQA6 18
DDR1_DQA7 18
DDR1_DQA8 18
DDR1_DQA9 18
DDR1_DQA10 18
DDR1_DQA11 18
DDR1_DQA12 18
DDR1_DQA13 18
DDR1_DQA14 18
DDR1_DQA15 18
DDR1_DMA0 18
DDR1_DMA1 18

DDR1_DQB0 18
DDR1_DQB1 18
DDR1_DQB2 18
DDR1_DQB3 18
DDR1_DQB4 18
DDR1_DQB5 18
DDR1_DQB6 18
DDR1_DQB7 18
DDR1_DQB8 18
DDR1_DQB9 18
DDR1_DQB10 18
DDR1_DQB11 18
DDR1_DQB12 18
DDR1_DQB13 18
DDR1_DQB14 18
DDR1_DQB15 18
DDR1_DMB0 18
DDR1_DMB1 18

DDR1_DQSA_T1 18
DDR1_DQSA_C1 18
DDR1_DQSA_T0 18
DDR1_DQSA_C0 18
DDR1_DQSB_T0 18
DDR1_DQSB_C0 18
DDR1_DQSB_T1 18
DDR1_DQSB_C1 18

DDR1_CLKA_T 18
DDR1_CLKA_C 18
DDR1_CLKB_T 18
DDR1_CLKB_C 18

DDR1_CAA0 18
DDR1_CAA1 18
DDR1_CAA2 18
DDR1_CAA3 18
DDR1_CAA4 18
DDR1_CAA5 18
DDR1_CAB0 18
DDR1_CAB1 18
DDR1_CAB2 18
DDR1_CAB3 18
DDR1_CAB4 18
DDR1_CAB5 18

DDR1_CSA0 18
DDR1_CSA1 18
DDR1_CSB0 18
DDR1_CSB1 18
DDR1_CKEA0 18
DDR1_CKEA1 18
DDR1_CKEB0 18
DDR1_CKEB1 18

VDD_DDR_1V1

R244 2K 1%

DDR1_RESET_N

VDD_DDR_0V6

R600 240R 1%

DDR1_RESET_N 18

MU1E

MIPI TX

MIPI_DSI1_P0 / VO0_D24 / I2S1_SCLK / GPIO185 / UART1_TX / SPI1_SDI / I2C8_SDA / VO1_D24
MIPI_DSI1_N0 / VO0_D25 / I2S1_WSI / GPIO186 / UART1_RX / SPI1_SDO / I2C8_SCL / VO1_D25
MIPI_DSI1_P1 / VO0_D26 / I2S1_SDI / GPIO187 / UART1_RTS / SPI1_SCK / I2C9_SDA / VO1_D26
MIPI_DSI1_N1 / VO0_D0 / I2S1_SDO / GPIO188 / UART1_CTS / SPI2_CS_X / I2C9_SCL / VO1_D0
MIPI_DSI1_P2 / VO0_D1 / I2S1_MCLK / GPIO189 / UART2_TX / SPI2_SDI / I2C1_SDA / VO1_D1
MIPI_DSI1_N2 / VO0_D2 / I2S2_MCLK / GPIO190 / UART2_RX / SPI2_SDO / I2C1_SCL / VO1_D2
MIPI_DSI1_P3 / VO0_D3 / I2S2_SCLK / GPIO124 / UART3_TX / SPI2_SCK / SPI3_SCK / VO1_D3
MIPI_DSI1_N3 / VO0_D4 / I2S2_WSI / GPIO125 / UART3_RX / SPI3_CS_X / SPI1_CS_X / VO1_D4
MIPI_DSI1_P4 / VO0_D5 / I2S2_SDI / GPIO126 / WG1_D1 / SPI3_SDI / I2C3_SDA / VO1_D5
MIPI_DSI1_N4 / VO0_D6 / I2S2_SDO / GPIO127 / WG1_D0 / SPI3_SDO / I2C3_SCL / VO1_D6

MIPI_DSI0_P0 / VO0_D14 / CAM_MCLK0 / GPIO174 / I2C5_SDA / VO1_D14
MIPI_DSI0_N0 / VO0_D15 / CAM_MCLK1 / GPIO175 / I2C5_SCL / VO1_D15
MIPI_DSI0_P1 / VO0_D16 / CAM_MCLK2 / GPIO176 / I2C6_SDA / VO1_D16
MIPI_DSI0_N1 / VO0_D17 / CAM_MCLK3 / GPIO177 / I2C6_SCL / VO1_D17
MIPI_DSI0_P2 / VO0_D18 / CAM_MCLK4 / GPIO178 / CAM_HS2 / I2C7_SDA / VO1_D18
MIPI_DSI0_N2 / VO0_D19 / CAM_MCLK5 / GPIO179 / CAM_VS2 / I2C7_SCL / VO1_D19
MIPI_DSI0_P3 / VO0_D20 / GPIO180 / CAM_HS0 / I2C8_SDA / VO1_D20
MIPI_DSI0_N3 / VO0_D21 / GPIO181 / CAM_VS0 / I2C8_SCL / VO1_D21
MIPI_DSI0_P4 / VO0_D22 / GPIO182 / CAM_HS1 / I2C9_SDA / VO1_D22
MIPI_DSI0_N4 / VO0_D23 / GPIO183 / CAM_VS1 / I2C9_SCL / VO1_D23

AN4 UART1_TXD >>> UART1_TXD 19
AP4 UART1_RXD >>> UART1_RXD 19
AN5 UART1_RTS* >>> UART1_RTS* 19
AP5 UART1_CTS* >>> UART1_CTS* 19
AN6
AP6
AN7
AP7
AN8 I2C3_SDA >>> I2C3_SDA 19
AP8 I2C3_SCL >>> I2C3_SCL 19

A10 GPIO174 >>> GPIO174 19
B10 GPIO175 >>> GPIO175 19
A9
B9
A8 GPIO178 >>> GPIO178 19
B8 GPIO179 >>> GPIO179 19
A7 GPIO180 >>> GPIO180 19
B7 GPIO181 >>> GPIO181 19
A6
B6

BM1688

MU1D

MIPI RX

BM1688

MIPI_CSI_0P / V12_CLK0 / GPIO138 / DEBUG_0
MIPI_CSI_0N / V12_D[8] / GPIO139 / DEBUG_1
MIPI_CSI_1P / V12_D[9] / GPIO140 / DEBUG_2
MIPI_CSI_1N / V12_D[10] / GPIO141 / DEBUG_3
MIPI_CSI_2P / V12_D[0] / GPIO142 / DEBUG_4
MIPI_CSI_2N / V12_D[1] / GPIO143 / DEBUG_5
MIPI_CSI_3P / V12_D[2] / V11_D[13] / GPIO144 / DEBUG_6 / V10_D[19] / IIC1_SDA
MIPI_CSI_3N / V12_D[3] / V11_D[12] / GPIO145 / DEBUG_7 / V10_D[20] / IIC1_SCL
MIPI_CSI_4P / V12_D[4] / V11_D[11] / GPIO146 / DEBUG_8 / V10_D[21]
MIPI_CSI_4N / V12_D[5] / V11_D[10] / GPIO147 / DEBUG_9 / V10_D[22]
MIPI_CSI_5P / V12_D[6] / V11_D[9] / GPIO148 / DEBUG_10 / V10_D[23] / CAM_MCLK0
MIPI_CSI_5N / V12_D[7] / V11_D[8] / GPIO149 / DEBUG_11 / V10_D[24] / CAM_MCLK1
MIPI_CSI_6P / V11_CLK0 / GPIO150 / DEBUG_12 / V10_CLK1 / CAM_MCLK2 / CAM_HS0
MIPI_CSI_6N / V11_D[14] / GPIO151 / DEBUG_13 / V10_D[25] / CAM_MCLK3 / CAM_VS0
MIPI_CSI_7P / V11_D[15] / GPIO152 / DEBUG_14 / V10_D[26] / CAM_HS1
MIPI_CSI_7N / V11_D[16] / GPIO153 / DEBUG_15 / V10_D[27] / CAM_VS1
MIPI_CSI_8P / V11_D[0] / GPIO154 / DEBUG_16 / V10_D[15] / CAM_HS2
MIPI_CSI_8N / V11_D[1] / GPIO155 / DEBUG_17 / V10_D[14] / CAM_VS2
MIPI_CSI_9P / V11_D[2] / GPIO156 / DEBUG_18 / V10_D[13] / IIC2_SDA
MIPI_CSI_9N / V11_D[3] / GPIO157 / DEBUG_19 / V10_D[12] / IIC2_SCL
MIPI_CSI_10P / V11_D[4] / GPIO158 / DEBUG_20 / V10_D[11] / IIC4_SDA / CAM_HS0
MIPI_CSI_10N / V11_D[5] / GPIO158 / DEBUG_21 / V10_D[10] / IIC4_SCL / CAM_VS0
MIPI_CSI_11P / V11_D[6] / GPIO160 / DEBUG_22 / V10_D[9] / CAM_MCLK0 / CAM_HS1
MIPI_CSI_11N / V11_D[7] / GPIO161 / DEBUG_23 / V10_D[8] / CAM_MCLK1 / CAM_VS1
MIPI_CSI_12P / GPIO162 / DEBUG_24 / V10_CLK0 / CAM_MCLK2 / CAM_HS2
MIPI_CSI_12N / GPIO163 / DEBUG_25 / V10_D[16] / CAM_MCLK3 / CAM_VS2
MIPI_CSI_13P / GPIO164 / DEBUG_26 / V10_D[17]
MIPI_CSI_13N / GPIO165 / DEBUG_27 / V10_D[18]
MIPI_CSI_14P / GPIO166 / DEBUG_28 / V10_D[0]
MIPI_CSI_14N / GPIO167 / DEBUG_29 / V10_D[1]
MIPI_CSI_15P / GPIO168 / DEBUG_30 / V10_D[2]
MIPI_CSI_15N / GPIO169 / DEBUG_31 / V10_D[3]
MIPI_CSI_16P / GPIO170 / V10_D[4]
MIPI_CSI_16N / GPIO171 / V10_D[5]
MIPI_CSI_17P / GPIO172 / V10_D[6] / CAM_MCLK4
MIPI_CSI_17N / GPIO173 / V10_D[7] / CAM_MCLK5

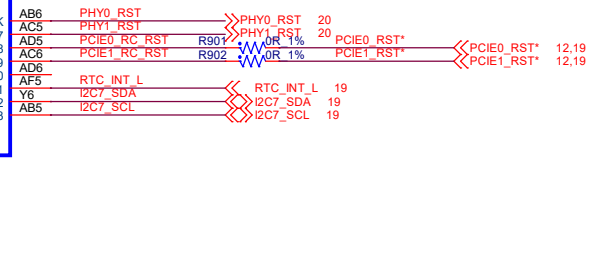
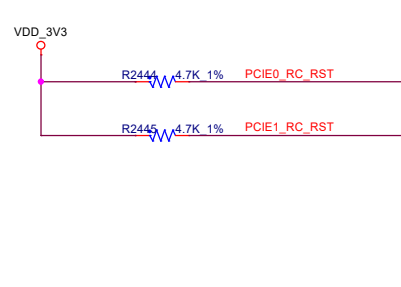
E33 CS10_D0_P >>> CS10_D0_P 19
E34 CS10_D0_N >>> CS10_D0_N 19
D33 CS10_D1_P >>> CS10_D1_P 19
D34 CS10_D1_N >>> CS10_D1_N 19
C33 CS10_CLK_P >>> CS10_CLK_P 19
C34 CS10_CLK_N >>> CS10_CLK_N 19
B33 CS11_D0_P >>> CS11_D0_P 19
B34 CS11_D0_N >>> CS11_D0_N 19
B32 CS11_D1_P >>> CS11_D1_P 19
A32 CS11_D1_N >>> CS11_D1_N 19
B31 CS11_CLK_P >>> CS11_CLK_P 19
A31 CS11_CLK_N >>> CS11_CLK_N 19
B30 CS12_D0_P >>> CS12_D0_P 19
A30 CS12_D0_N >>> CS12_D0_N 19
D30 CS12_D1_P >>> CS12_D1_P 19
C30 CS12_D1_N >>> CS12_D1_N 19
B29 CS12_CLK_P >>> CS12_CLK_P 19
A29 CS12_CLK_N >>> CS12_CLK_N 19
D29 CS13_D0_P >>> CS13_D0_P 19
C29 CS13_D0_N >>> CS13_D0_N 19
B28 CS13_D1_P >>> CS13_D1_P 19
A28 CS13_D1_N >>> CS13_D1_N 19
D28 CS13_CLK_P >>> CS13_CLK_P 19
C28 CS13_CLK_N >>> CS13_CLK_N 19
B27 CS14_D0_P >>> CS14_D0_P 19
A27 CS14_D0_N >>> CS14_D0_N 19
D27 CS14_D1_P >>> CS14_D1_P 19
C27 CS14_D1_N >>> CS14_D1_N 19
B26 CS14_CLK_P >>> CS14_CLK_P 19
A26 CS14_CLK_N >>> CS14_CLK_N 19
D26 CS15_D0_P >>> CS15_D0_P 19
C26 CS15_D0_N >>> CS15_D0_N 19
B25 CS15_D1_P >>> CS15_D1_P 19
A25 CS15_D1_N >>> CS15_D1_N 19
D25 CS15_CLK_P >>> CS15_CLK_P 19
C25 CS15_CLK_N >>> CS15_CLK_N 19

F8 CAM0_MCLK >>> CAM0_MCLK 19
E7 CAM1_MCLK >>> CAM1_MCLK 19
C5 >>> >>>
D6 >>> >>>
D7 >>> CAM2_MCLK 19
G9 CAM3_MCLK >>> CAM3_MCLK 19
F9 CAM0_PWDN >>> CAM0_PWDN 19
G10 CAM1_PWDN >>> CAM1_PWDN 19

P-V0

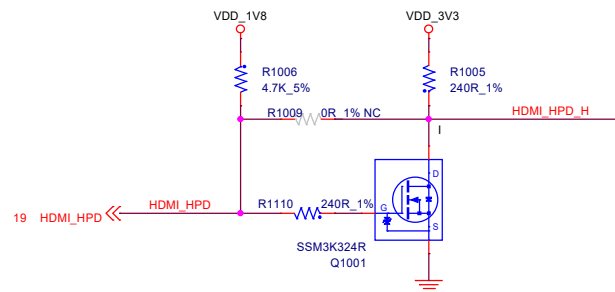
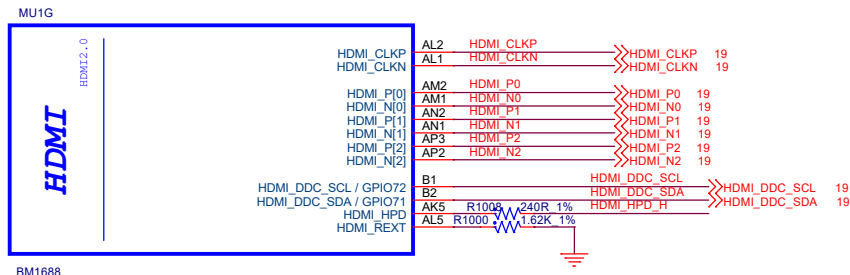
3.3V and 1.8V AREA

GPIO184 / VO0_CLK / UART4_TX / IIC0_SDA / VO1_CLK
 GPIO128 / VO0_D7 / UART4_RX / IIC0_SCL / VO1_D7
 GPIO129 / I2S0_SCL / VO0_D8 / UART5_TX / SPI0_CS_X / IIC3_SDA / VO1_D8
 GPIO130 / I2S0_WS1 / VO0_D9 / UART5_RX / SPI0_SDI / IIC5_SCL / VO1_D9
 GPIO131 / I2S0_SDI0 / VO0_D10 / UART6_TX / SPI0_SDO / IIC6_SDA / VO1_D10
 GPIO132 / I2S0_SDI1 / VO0_D11 / UART6_RX / SPI0_SCK / IIC6_SCL / VO1_D11
 GPIO133 / I2S0_SDO / CAN1_TX / VO0_D12 / UART7_TX / UART4_RTS / IIC7_SDA / VO1_D12
 GPIO134 / I2S0_MCLK / CAN1_RXD / VO0_D13 / UART7_RX / UART4_CTS / IIC7_SCL / VO1_D13

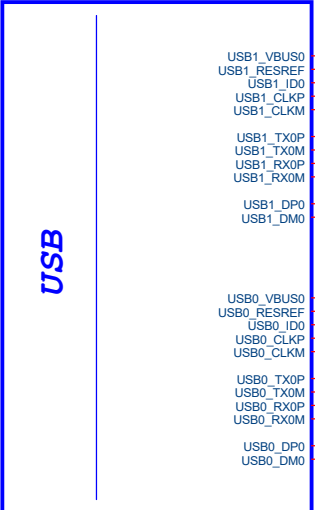


```
RC模式：
PCIE0:  R901上件,  R1207下件
PCIE1:  R902上件,  R1208下件

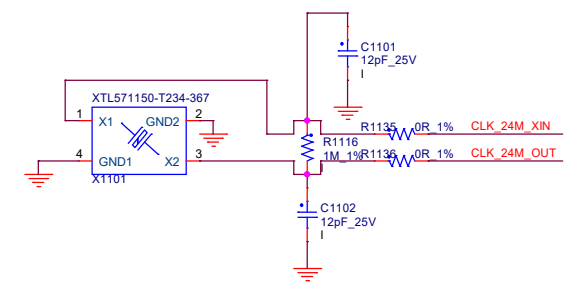
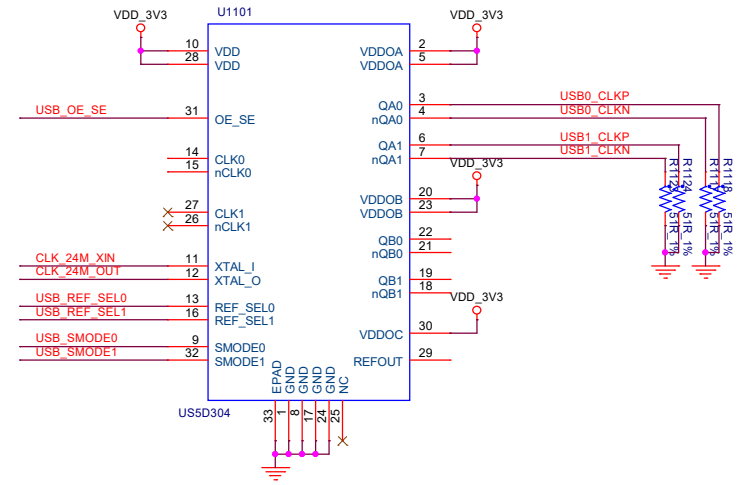
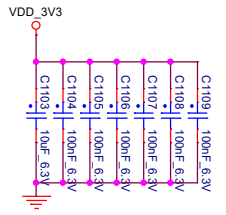
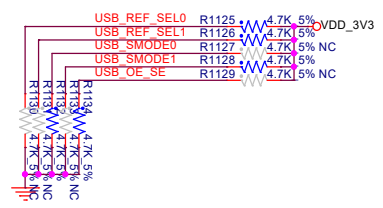
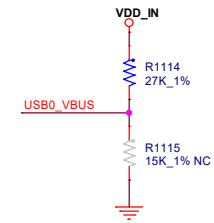
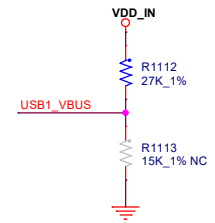
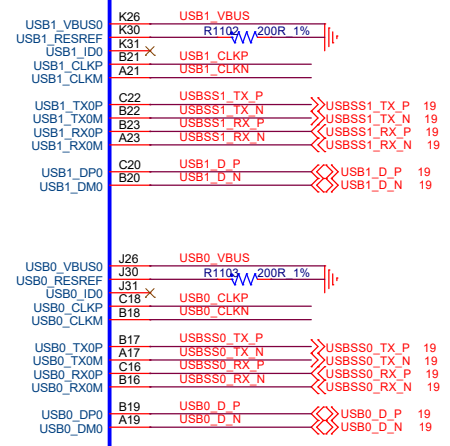
EF模式：
PCIE0:  R901下件,  R1207上件
PCIE1:  R902下件,  R1208上件
```



MU1H



BM1688



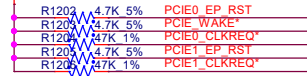
MU11

3.3V and 1.8V AREA

PCIE_SATA

BM1688

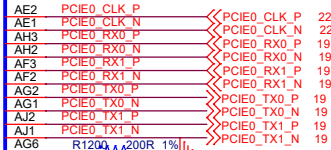
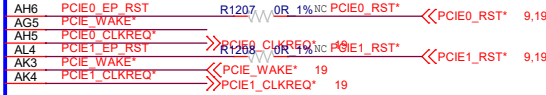
VDD_3V3



PCIE0_L0_RESET_X / GPIO40
PCIE0_L0_WAKEUP_X / GPIO41 / UART2_RTS / I2C4_SDA
PCIE0_L0_CLKREQ_IN_X / GPIO42 / UART2_CTS / I2C4_SCL
PCIE1_L0_RESET_X / GPIO43 / CAN1_TX / UART2_TX / I2C7_SDA
PCIE1_L0_WAKEUP_X / SATA0_DEVSLP / GPIO44 / CAN1_RX / UART2_RX / I2C7_SCL
PCIE1_L0_CLKREQ_IN_X / SATA1_DEVSLP / GPIO45

PCIE0_CLK_P
PCIE0_CLK_N
PCIE0_RX0_P / SATA_RX0_P
PCIE0_RX0_N / SATA_RX0_N
PCIE0_RX1_P / SATA_RX1_P
PCIE0_RX1_N / SATA_RX1_N
PCIE0_TX0_P / SATA_TX0_P
PCIE0_TX0_N / SATA_TX0_N
PCIE0_TX1_P / SATA_TX1_P
PCIE0_TX1_N / SATA_TX1_N
PCIE0_RESREF

PCIE1_CLK_P
PCIE1_CLK_N
PCIE1_RX0_P
PCIE1_RX0_N
PCIE1_RX1_P
PCIE1_RX1_N
PCIE1_TX0_P
PCIE1_TX0_N
PCIE1_TX1_P
PCIE1_TX1_N
PCIE1_RESREF



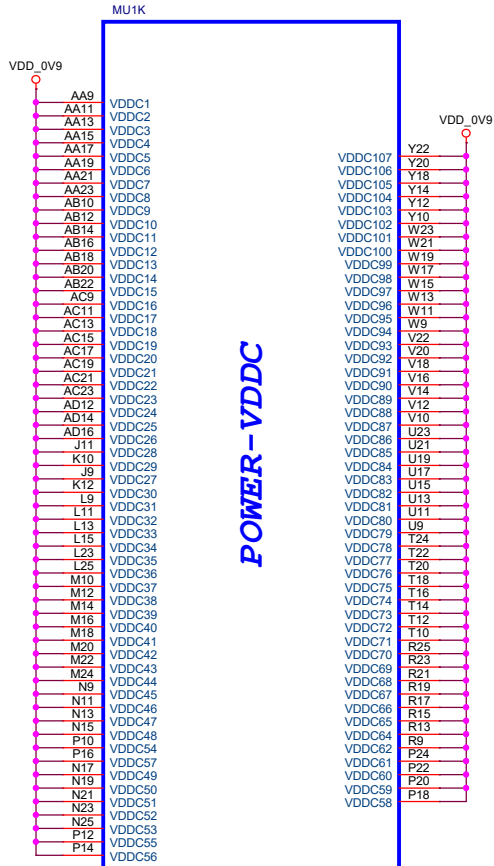
RC模式:
PCIE0: R901上件, R1207下件
PCIE1: R902上件, R1208下件
EP模式:
PCIE0: R901下件, R1207上件
PCIE1: R902下件, R1208上件

MU1J

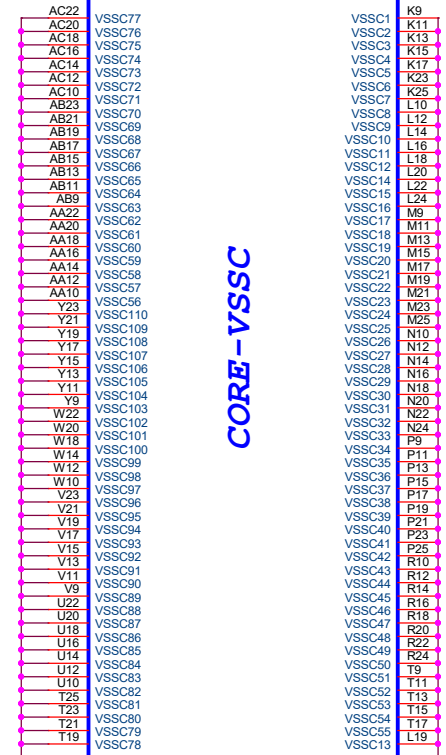
GMAC

BM1688

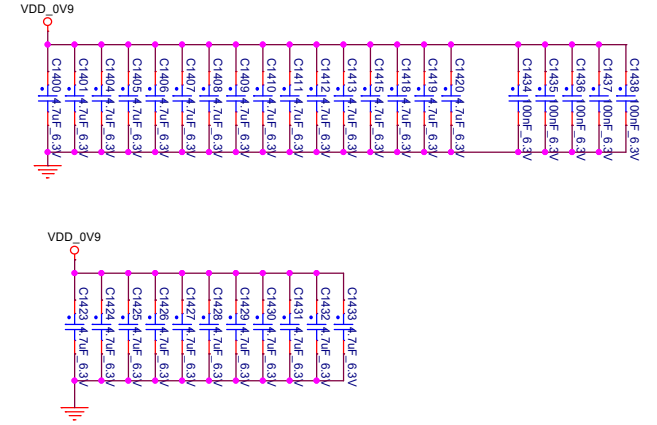
RGMII0_MDC / PWM18 / GPIO22	P2	RGMII0_MDC	>>RGMII0_MDC	20
RGMII0_MDIO / PWM19 / GPIO23	R2	RGMII0_MDIO	>>RGMII0_MDIO	20
RGMII0_IRQ / PWM17 / GPIO21	T2	PHY0_RGMII_IRQ	>>PHY0_RGMII_IRQ	20
RGMII0_REFCLK0 / PWM16 / GPIO20	U2	R1304 RGMII0_REFCLK0	>>RGMII0_REFCLK0	20
RGMII0_RXC / PWM15 / GPIO19	N2	RGMII0_RXC	>>RGMII0_RXC	20
RGMII0_RXCTRL / PWM13 / GPIO17	L2	RGMII0_RXCTRL	>>RGMII0_RXCTRL	20
RGMII0_RXD0 / PWM9 / GPIO13	U1	RGMII0_RXD0	>>RGMII0_RXD0	20
RGMII0_RXD1 / PWM10 / GPIO14	M1	RGMII0_RXD1	>>RGMII0_RXD1	20
RGMII0_RXD2 / PWM11 / GPIO15	N1	RGMII0_RXD2	>>RGMII0_RXD2	20
RGMII0_RXD3 / PWM12 / GPIO16	V2	RGMII0_RXD3	>>RGMII0_RXD3	20
RGMII0_TXC / PWM14 / GPIO18	M2	R1305 RGMII0_TXC	>>RGMII0_TXC	20
RGMII0_TXCTRL / PWM8 / GPIO12	V1	RGMII0_TXCTRL	>>RGMII0_TXCTRL	20
RGMII0_TXD0 / PWM4 / GPIO8	W1	RGMII0_TXD0	>>RGMII0_TXD0	20
RGMII0_TXD1 / PWM5 / GPIO9	P1	RGMII0_TXD1	>>RGMII0_TXD1	20
RGMII0_TXD2 / PWM6 / GPIO10	R1	RGMII0_TXD2	>>RGMII0_TXD2	20
RGMII0_TXD3 / PWM7 / GPIO11	T1	RGMII0_TXD3	>>RGMII0_TXD3	20
RGMII1_MDC / PWM18 / GPIO38 / I2C9_SDA	R4	RGMII1_MDC	>>RGMII1_MDC	20
RGMII1_MDIO / PWM19 / GPIO39 / I2C9_SCL	P4	RGMII1_MDIO	>>RGMII1_MDIO	20
RGMII1_IRQ / PWM17 / GPIO37 / I2C8_SCL	P3	PHY1_RGMII_IRQ	>>PHY1_RGMII_IRQ	20
RGMII1_REFCLK0 / PWM16 / GPIO36 / I2C8_SDA	M4	R1313 RGMII1_REFCLK0	>>RGMII1_REFCLK0	20
RGMII1_RXC / PWM15 / GPIO35 / I2C6_SCL	T4	RGMII1_RXC	>>RGMII1_RXC	20
RGMII1_RXCTRL / PWM13 / GPIO33 / I2C5_SCL	T3	RGMII1_RXCTRL	>>RGMII1_RXCTRL	20
RGMII1_RXD0 / PWM9 / GPIO29 / I2C3_SCL	V3	RGMII1_RXD0	>>RGMII1_RXD0	20
RGMII1_RXD1 / PWM10 / GPIO30 / I2C4_SDA	U3	RGMII1_RXD1	>>RGMII1_RXD1	20
RGMII1_RXD2 / PWM11 / GPIO31 / I2C4_SCL	L3	RGMII1_RXD2	>>RGMII1_RXD2	20
RGMII1_RXD3 / PWM12 / GPIO32 / I2C5_SDA	M3	RGMII1_RXD3	>>RGMII1_RXD3	20
RGMII1_TXC / PWM14 / GPIO34 / I2C6_SDA	R3	R1314 RGMII1_TXC	>>RGMII1_TXC	20
RGMII1_TXCTRL / PWM8 / GPIO28 / I2C3_SDA	V4	RGMII1_TXCTRL	>>RGMII1_TXCTRL	20
RGMII1_TXD0 / PWM4 / GPIO24 / I2C1_SDA	W5	RGMII1_TXD0	>>RGMII1_TXD0	20
RGMII1_TXD1 / PWM5 / GPIO25 / I2C1_SCL	Y5	RGMII1_TXD1	>>RGMII1_TXD1	20
RGMII1_TXD2 / PWM6 / GPIO26 / I2C2_SDA	W6	RGMII1_TXD2	>>RGMII1_TXD2	20
RGMII1_TXD3 / PWM7 / GPIO27 / I2C2_SCL	V5	RGMII1_TXD3	>>RGMII1_TXD3	20

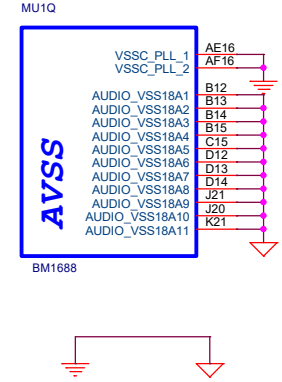
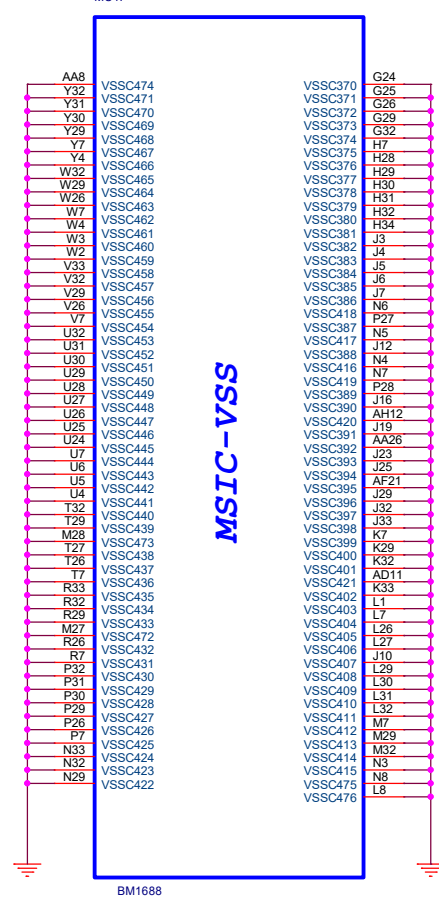
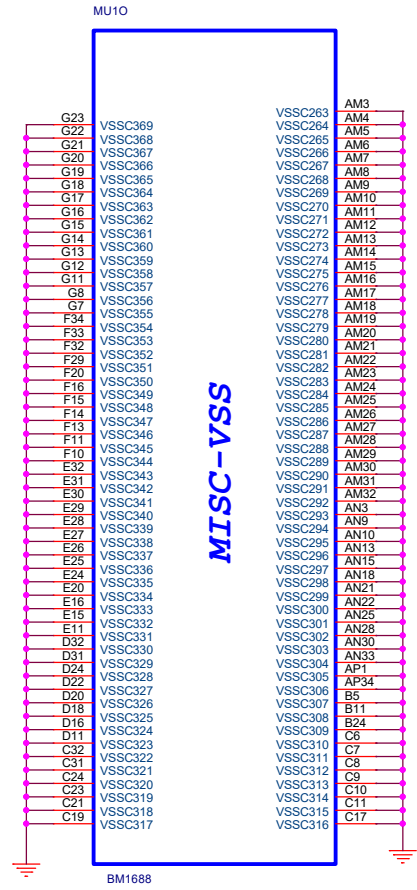
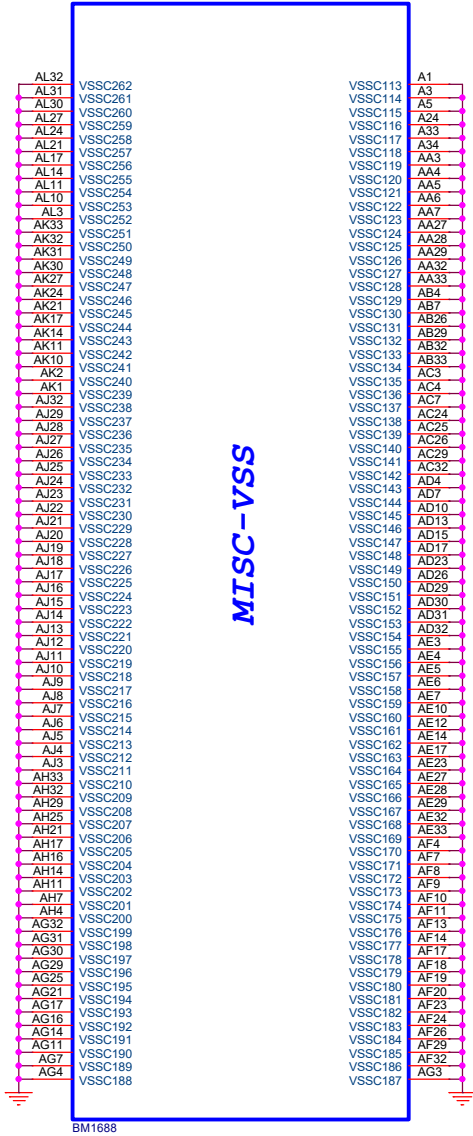


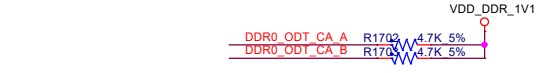
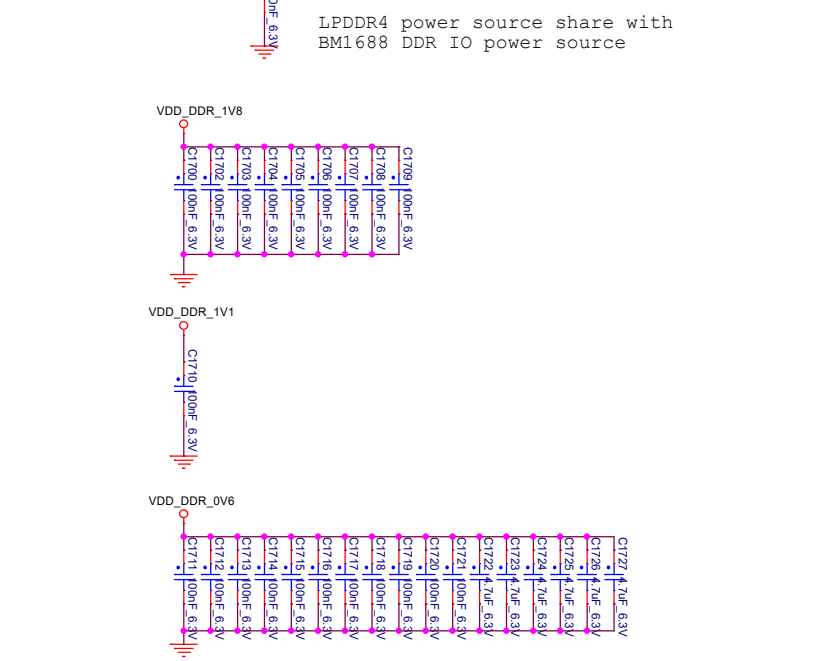
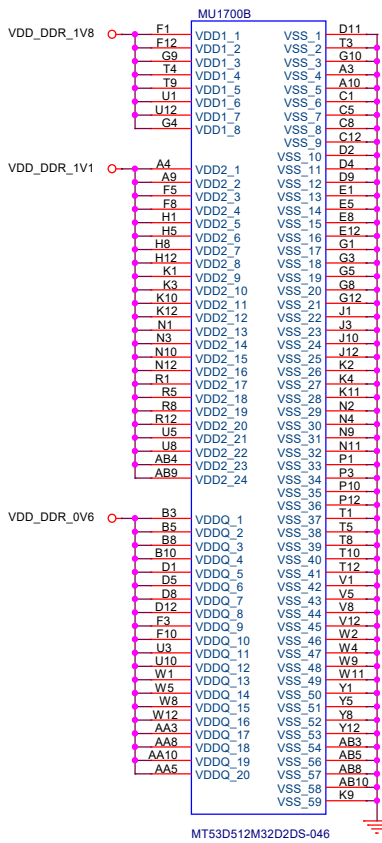
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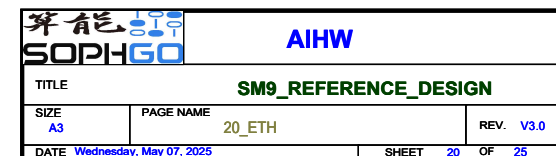


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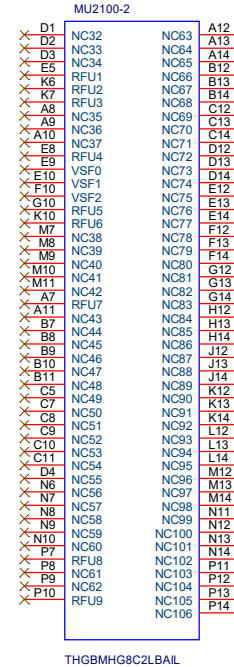
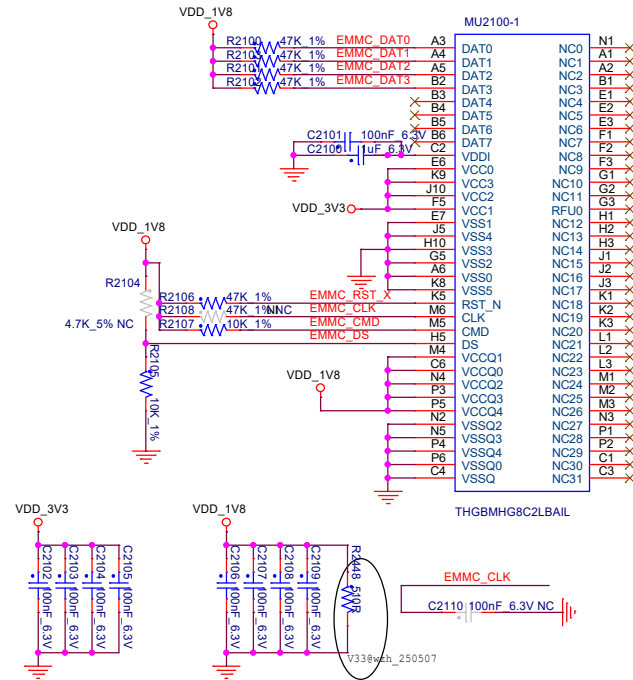






5 EMMC_DAT0
5 EMMC_DAT1
5 EMMC_DAT2
5 EMMC_DAT3
5 EMMC_CLK
5 EMMC_CMD
5 EMMC_RST_X

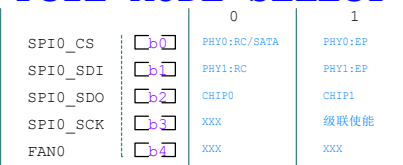
EMMC_DAT0
EMMC_DAT1
EMMC_DAT2
EMMC_DAT3
EMMC_CLK
EMMC_CMD
EMMC_RST_X



DDR Configuration



PCIE MODE SELECT



	OTP	GPIO117			ADC3			功能定义	备注	
	OTP	GPIO117真值	GPIO117上拉电阻kΩ	MIN/mV	Typ/mV	MAX/mV	上拉电阻R1kΩ			下拉电阻R2kΩ
range0	0x11/cv186ah	0	NC	0	0	105	NC	0	LP4 2R 1S	适配AC
range1	0x11/cv186ah	0	NC	195	300	405	7.5	1.5	LP4 1R 2S	
range2	0x11/cv186ah	0	NC	495	600	705	15	7.5	LP4 2R 2S	
range3	0x11/cv186ah	0	NC	795	900	1005	4.7	4.7	LP4 1R 1S	
range4	0x11/cv186ah	0	NC	1095	1200	1305	7.5	15	LP4X 2R 2S	
range5	0x11/cv186ah	0	NC	1395	1500	1500	1.5	7.5	LP4X 1R 2S	
range6	0x11/cv186ah	1	1	0	0	105	NC	0	DDR4 1R 2S	
range7	0x11/cv186ah	1	1	195	300	405	7.5	1.5	LP4X 1R 1S	
range8	0x11/cv186ah	1	1	495	600	705	15	7.5	DDR4 1R 1S	
range9	0x11/cv186ah	1	1	795	900	1005	4.7	4.7	LP4X 2R 1S	
range10	0x11/cv186ah	1	1	1095	1200	1305	7.5	15		适配AC
range11	0x11/cv186ah	1	1	1395	1500	1500	1.5	7.5		
range12	0x00/bm1688	0	NC	0	0	105	NC	0	LP4 2R 2S	
range13	0x00/bm1688	0	NC	195	300	405	7.5	1.5	LP4 1R 2S	
range14	0x00/bm1688	0	NC	495	600	705	15	7.5	LP4 2R 1S	
range15	0x00/bm1688	0	NC	795	900	1005	4.7	4.7	LP4 1R 1S	
range16	0x00/bm1688	0	NC	1095	1200	1305	7.5	15	LP4X 2R 2S	
range17	0x00/bm1688	0	NC	1395	1500	1500	1.5	7.5	LP4X 1R 2S	
range18	0x00/bm1688	1	1	0	0	105	NC	0	DDR4 1R 2S	
range19	0x00/bm1688	1	1	195	300	405	7.5	1.5	LP4X 1R 1S	
range20	0x00/bm1688	1	1	495	600	705	15	7.5	DDR4 1R 1S	
range21	0x00/bm1688	1	1	795	900	1005	4.7	4.7	LP4X 2R 1S	
range22	0x00/bm1688	1	1	1095	1200	1305	7.5	15		
range23	0x00/bm1688	1	1	1395	1500	1500	1.5	7.5		

