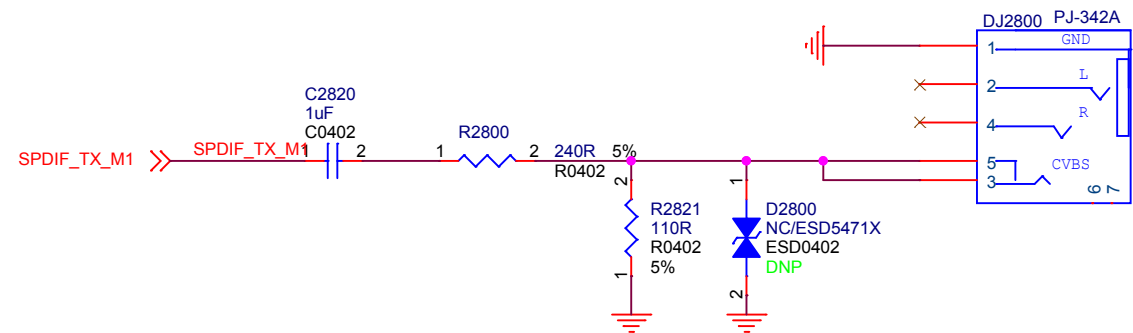
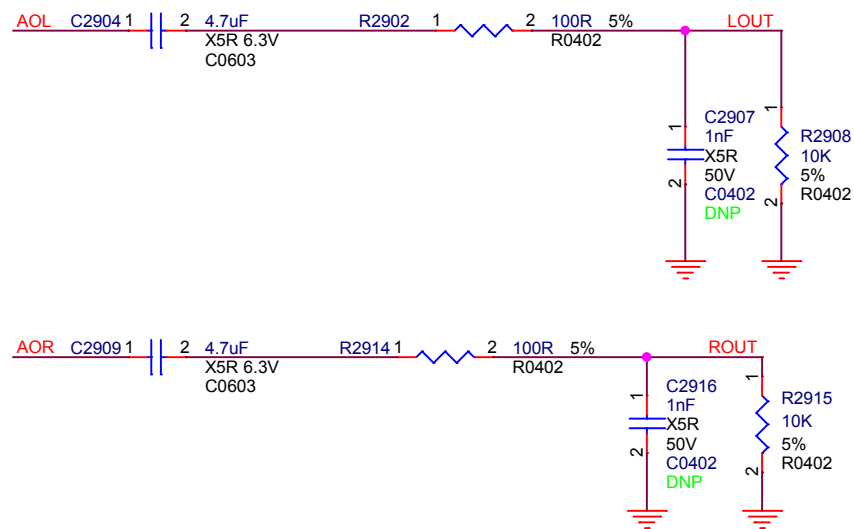
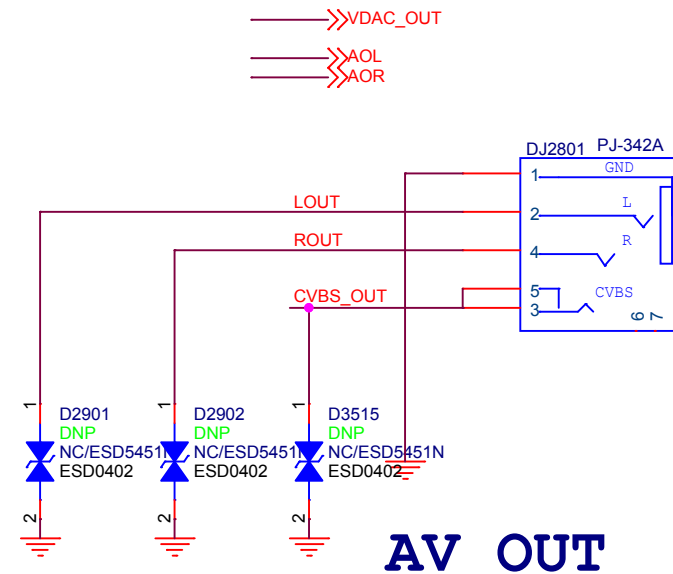
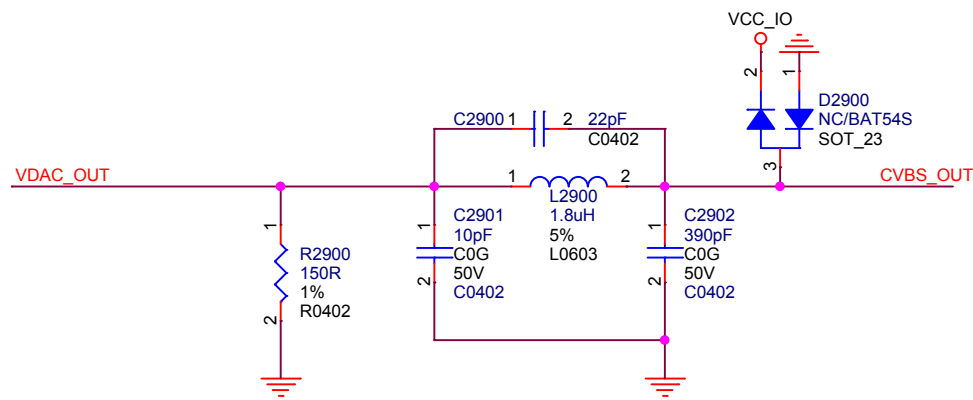




ShenZhen HugSun Technology Co., Ltd			
Project:	MXQ_RK3328_D16		
File:	27.Debug UART2		
Date:	Wednesday, December 19, 2018	Rev:	V0.9
Designed by:	Wang	Sheet:	0 of 0



2-Vrms Audio Line Driver

ShenZhen HugSun Technology Co., Ltd

Project: MXQ_RK3328_D16

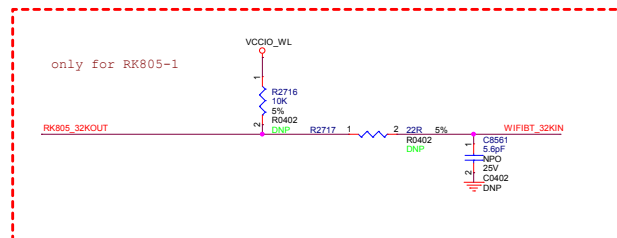
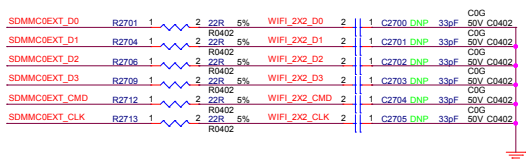
File: 23.AV OUT Port

Date: Wednesday, December 19, 2018

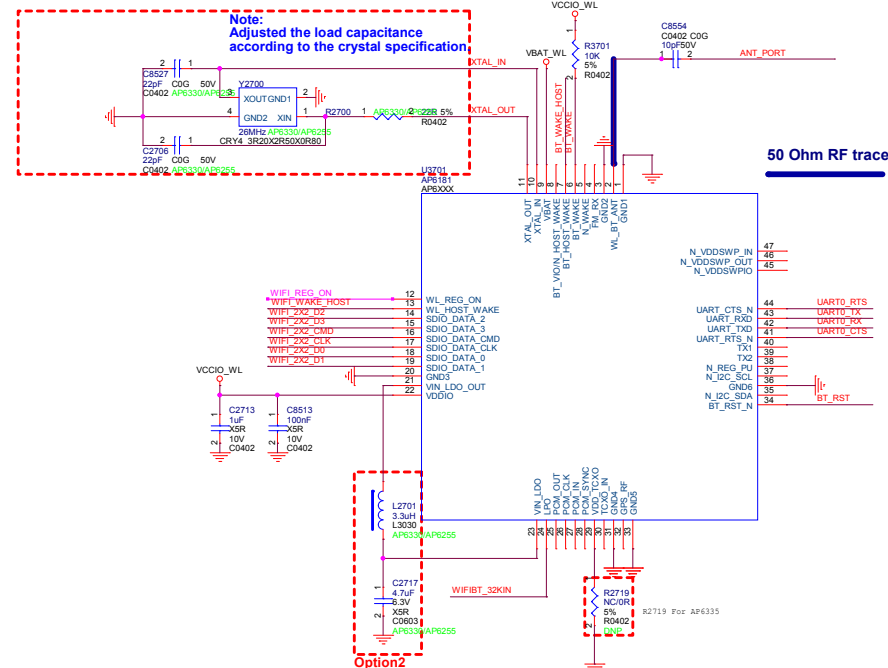
Rev: V0.9

Designed by: Wang

Sheet: 0 of 0



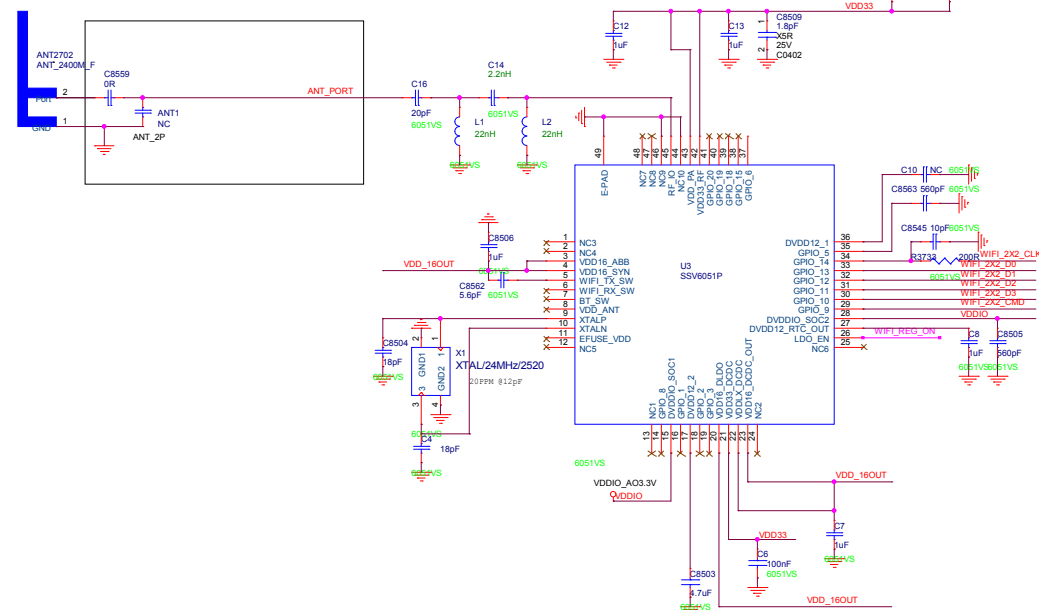
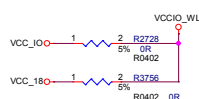
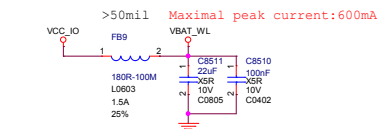
Option4

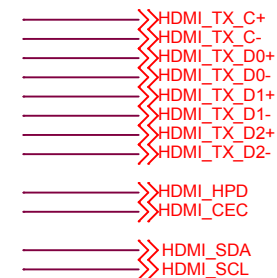
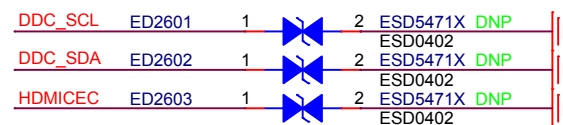
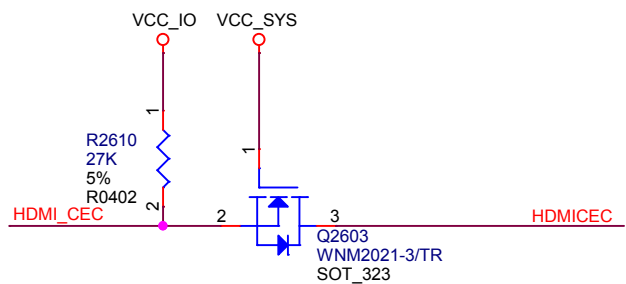


```
1X1  WIFI      AP6181/RTL8189ETV
```

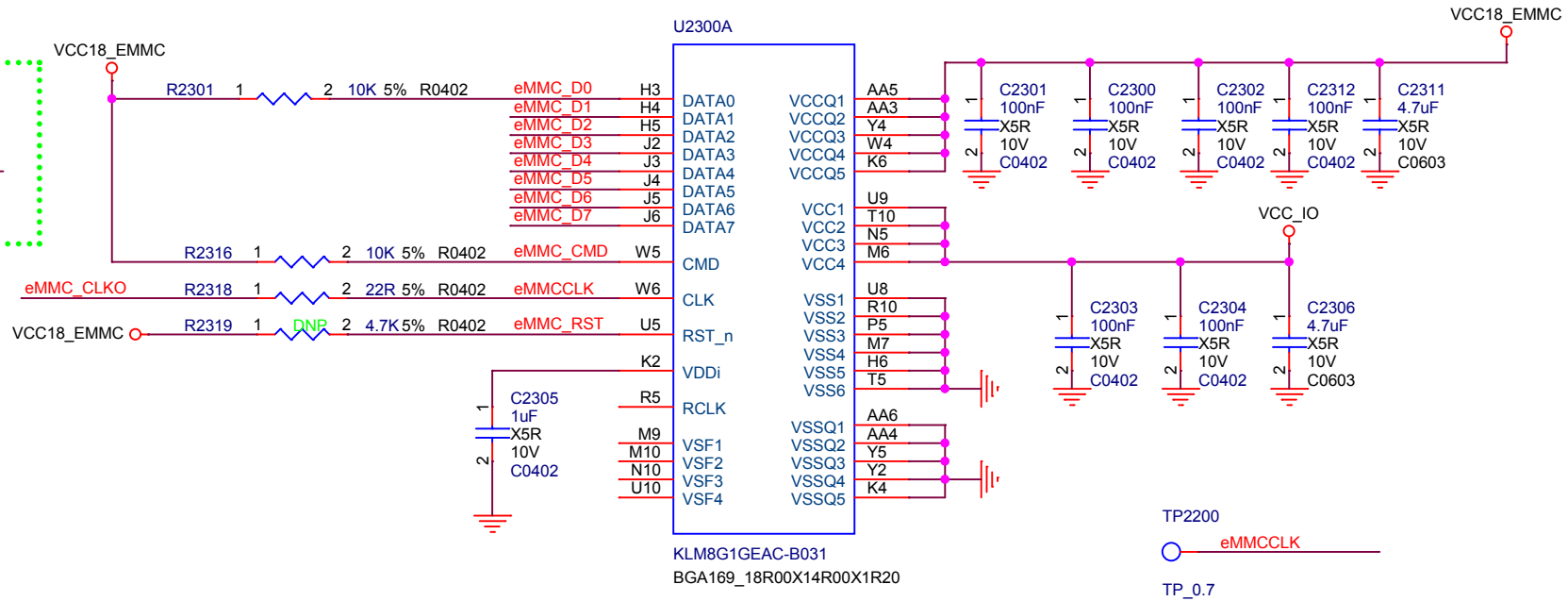
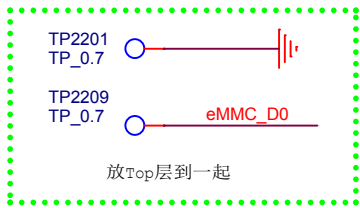
OPTION	WIFI				BT	Crystals	VDDIO
	a	b/g/n	ac	5GHz			
AP6330	No	Yes	No	Yes	Yes	26MHz 16pF	1.8V
RTL8189ETV MODULE	No	Yes	No	No	No	40MHz On the module	3.3V
AP6330		Yes	Yes	Yes	Yes	37.4MHz 10ppm, 12pF	1.8V

OPTION	1	2	4
AP6181	Yes	Yes	Yes
RTL8189ETV MODULE	No	No	No



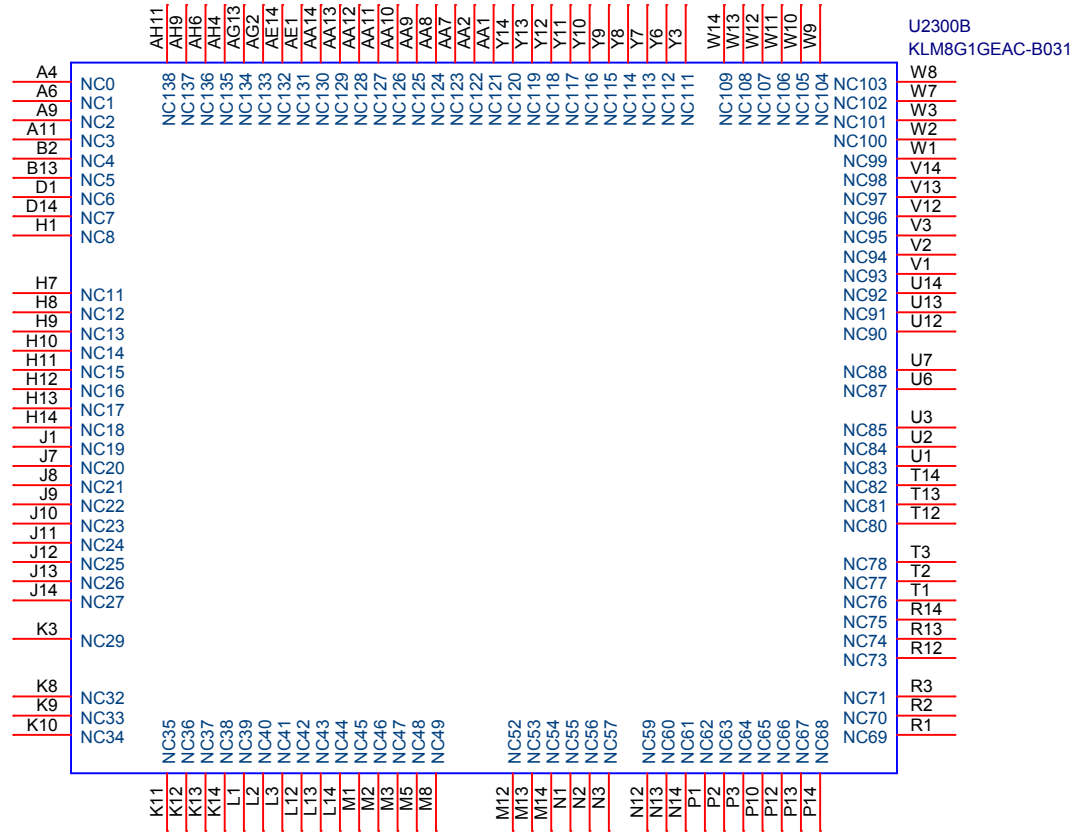
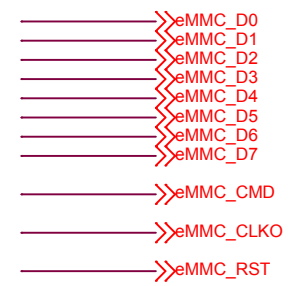






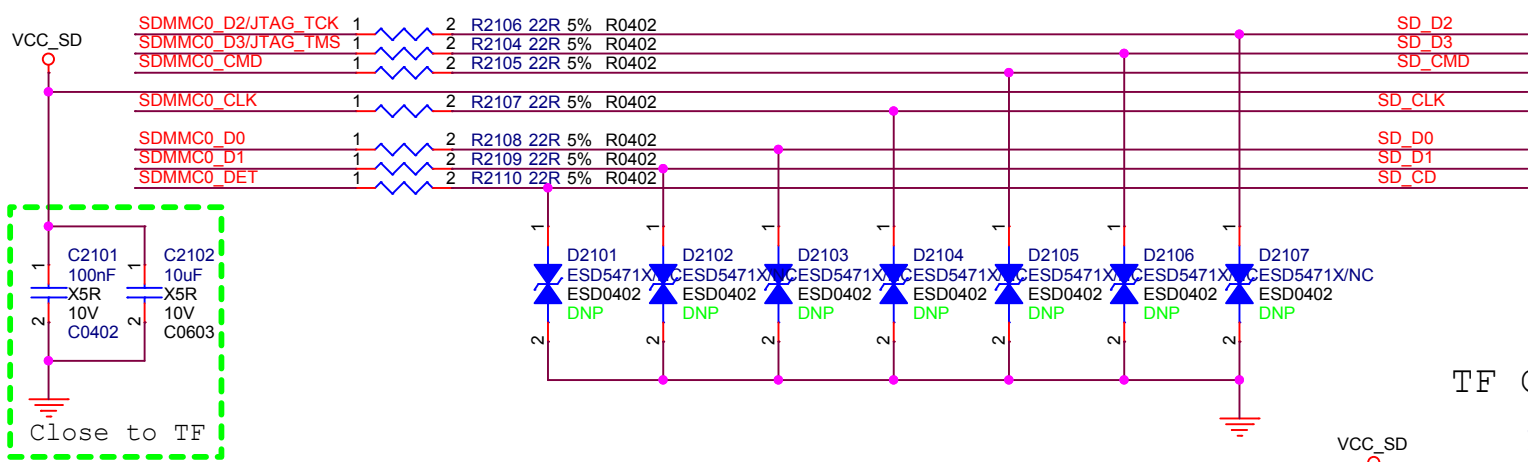
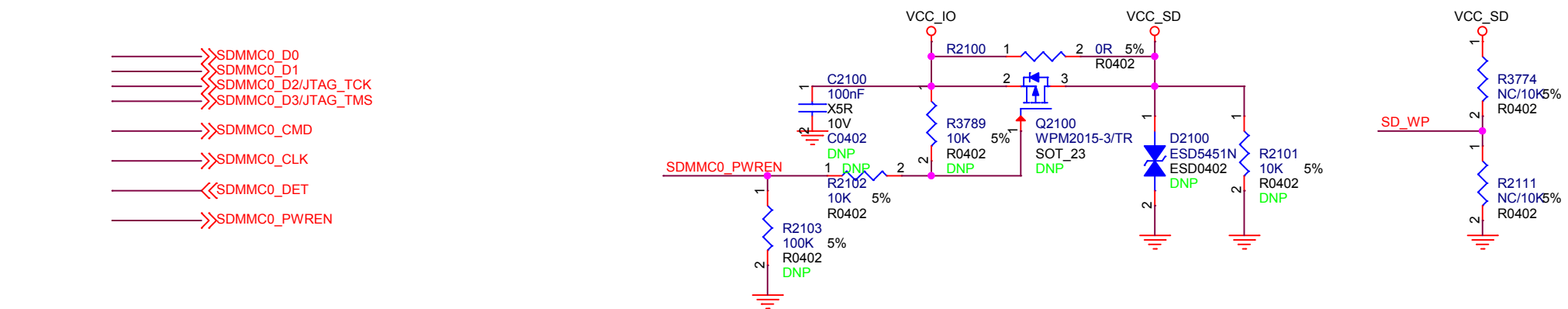
KLM8G1GEAC-B031
BGA169_18R00X14R00X1R20

Note:
eMMC Update.

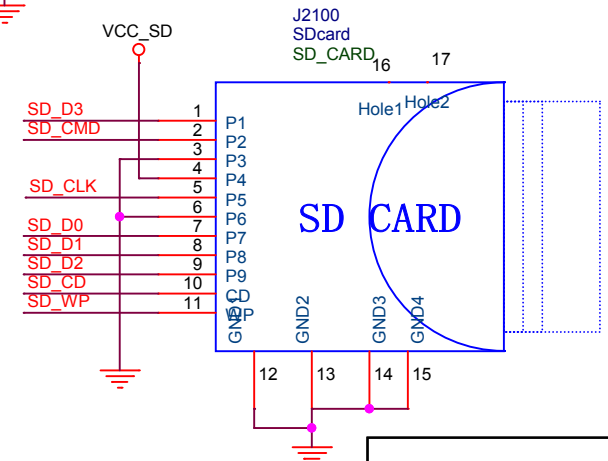


BGA169_18R00X14R00X1R20

ShenZhen HugSun Technology Co., Ltd					
Project:	MXQ_RK3328_D16				
File:	18.Flash-eMMC				
Date:	Wednesday, December 19, 2018			Rev:	V0.9
Designed by:	Wang			Sheet:	0 of 0

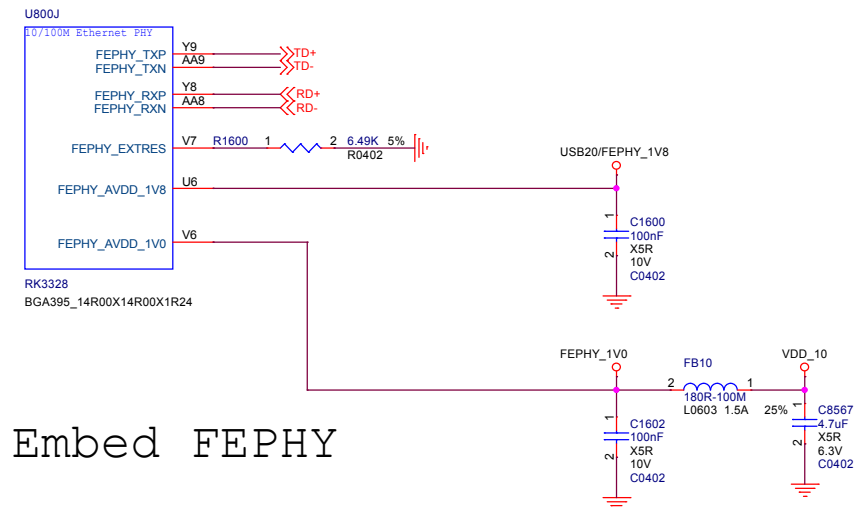
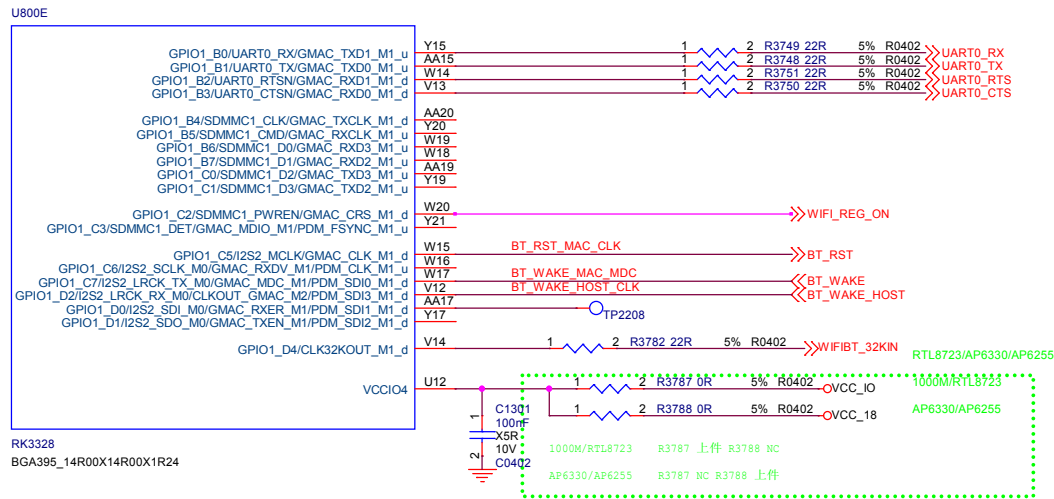


TF Card



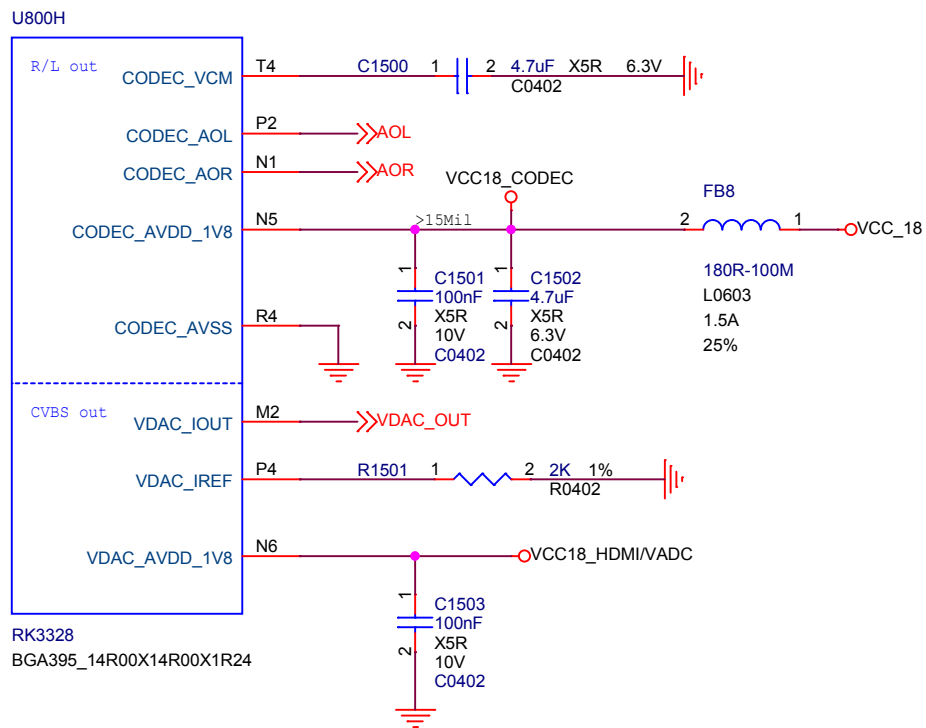
ShenZhen HugSun Technology Co., Ltd			
Project:	MXQ_RK3328_D16		
File:	17.TF/SD Card		
Date:	Wednesday, December 19, 2018	Rev:	V0.9
Designed by:	Wang	Sheet:	0 of 0

10/100/1000M RGMII



Embed FEPHY

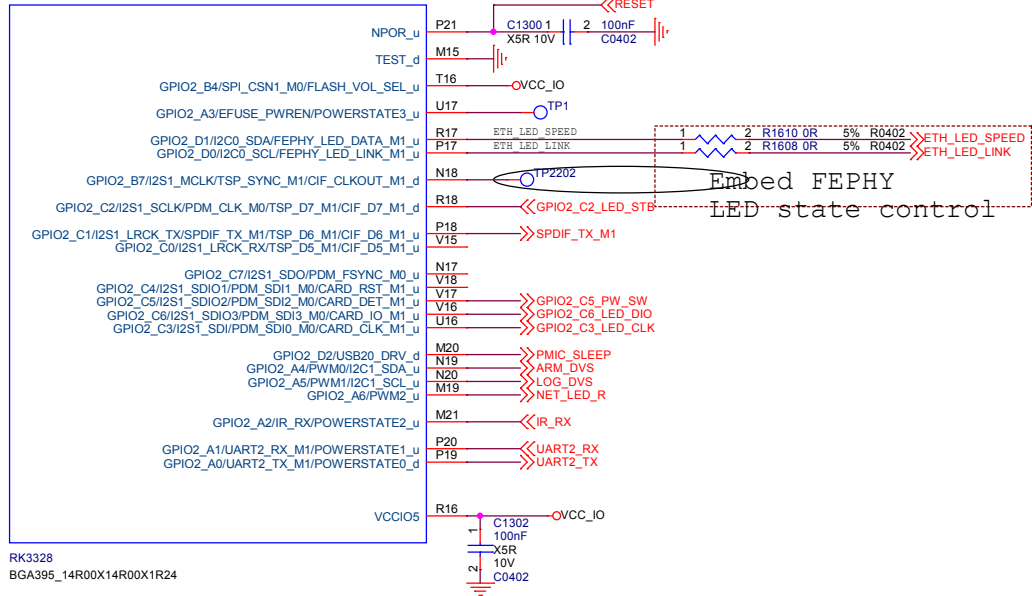
10/100M/1000M RGMII Interface		
MAC_TXD0	>	PHY_TXD0
MAC_TXD1	>	PHY_TXD1
MAC_TXD2	>	PHY_TXD2
MAC_TXD3	>	PHY_TXD3
MAC_TXEN	>	PHY_TXEN
MAC_TXCLK	>	PHY_TXCLK
MAC_RXD0	<.....	PHY_RXD0
MAC_RXD1	<.....	PHY_RXD1
MAC_RXD2	<.....	PHY_RXD2
MAC_RXD3	<.....	PHY_RXD3
MAC_RXDV	<.....	PHY_RXDV
MAC_RXCLK	<.....	PHY_RXCLK
MAC_CLK	<.....	PHY_CLKOUT125
MAC_MDIO	<.....>	PHY_MDIO
MAC_MDC	>	PHY_MDC
PHY_RST	>	PHY_RST



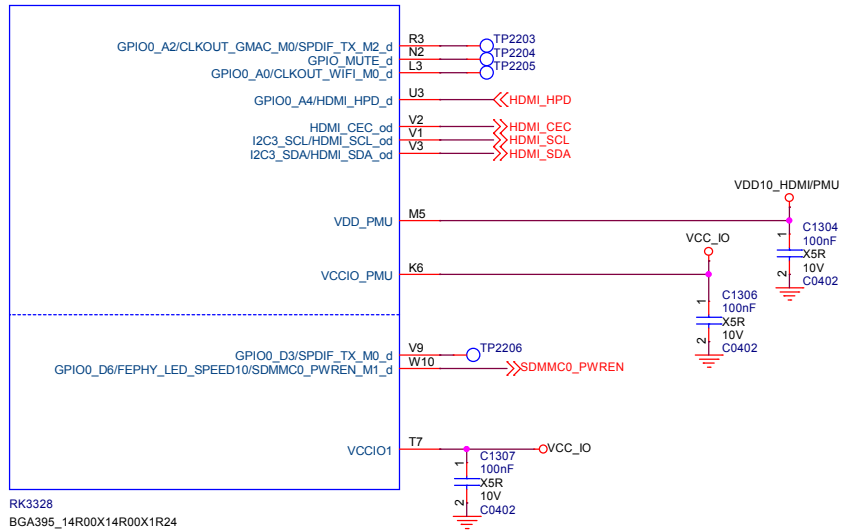
ShenZhen HugSun Technology Co., Ltd			
Project:	MXQ_RK3328_D16		
File:	13.RK3328 AV Interface		
Date:	Wednesday, December 19, 2018	Rev:	V0.9
Designed by:	Wang	Sheet:	0 of 0

BT_WAKE
BT_WAKE_HOST
BT_RST

U800F

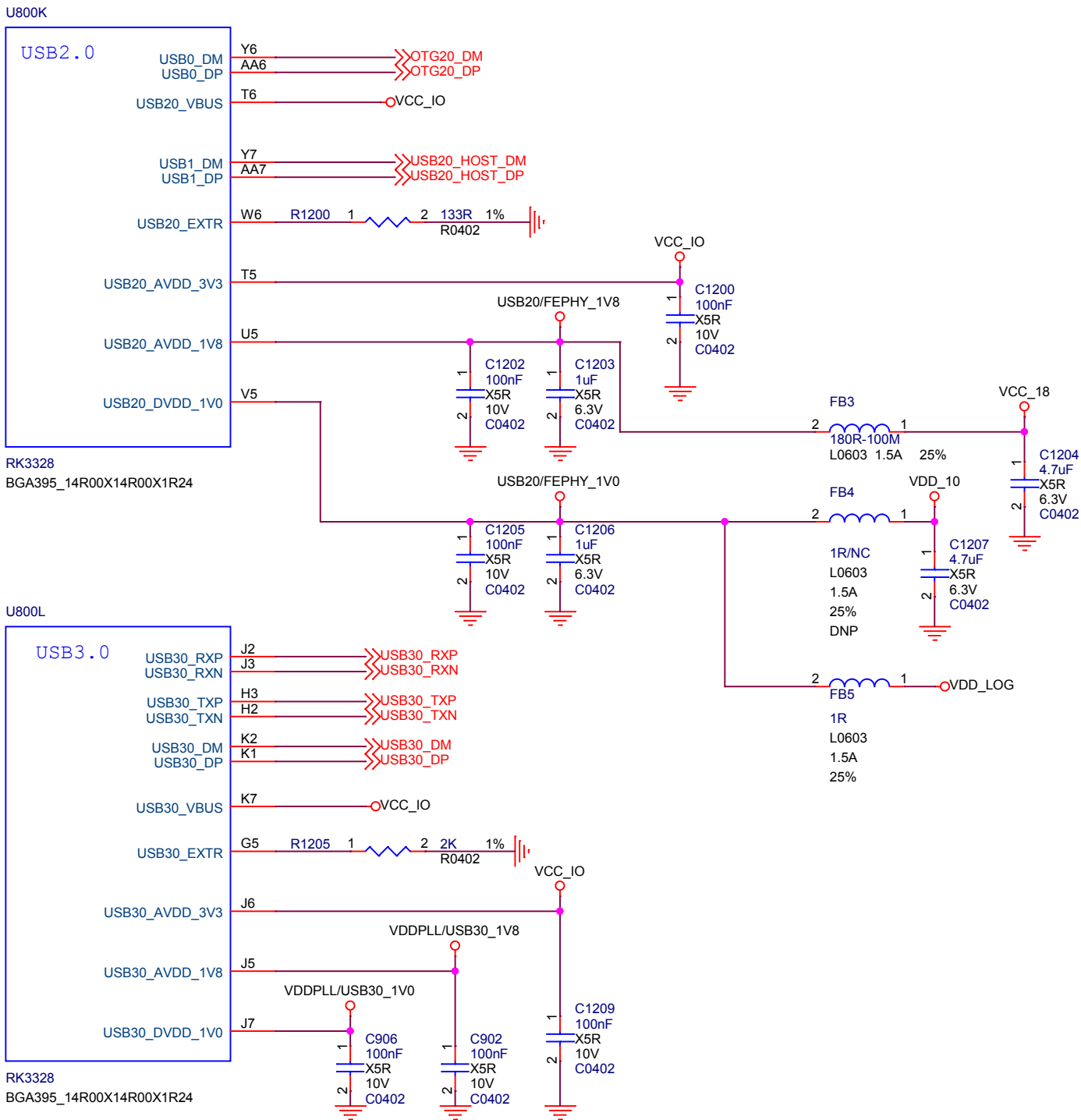


U800B

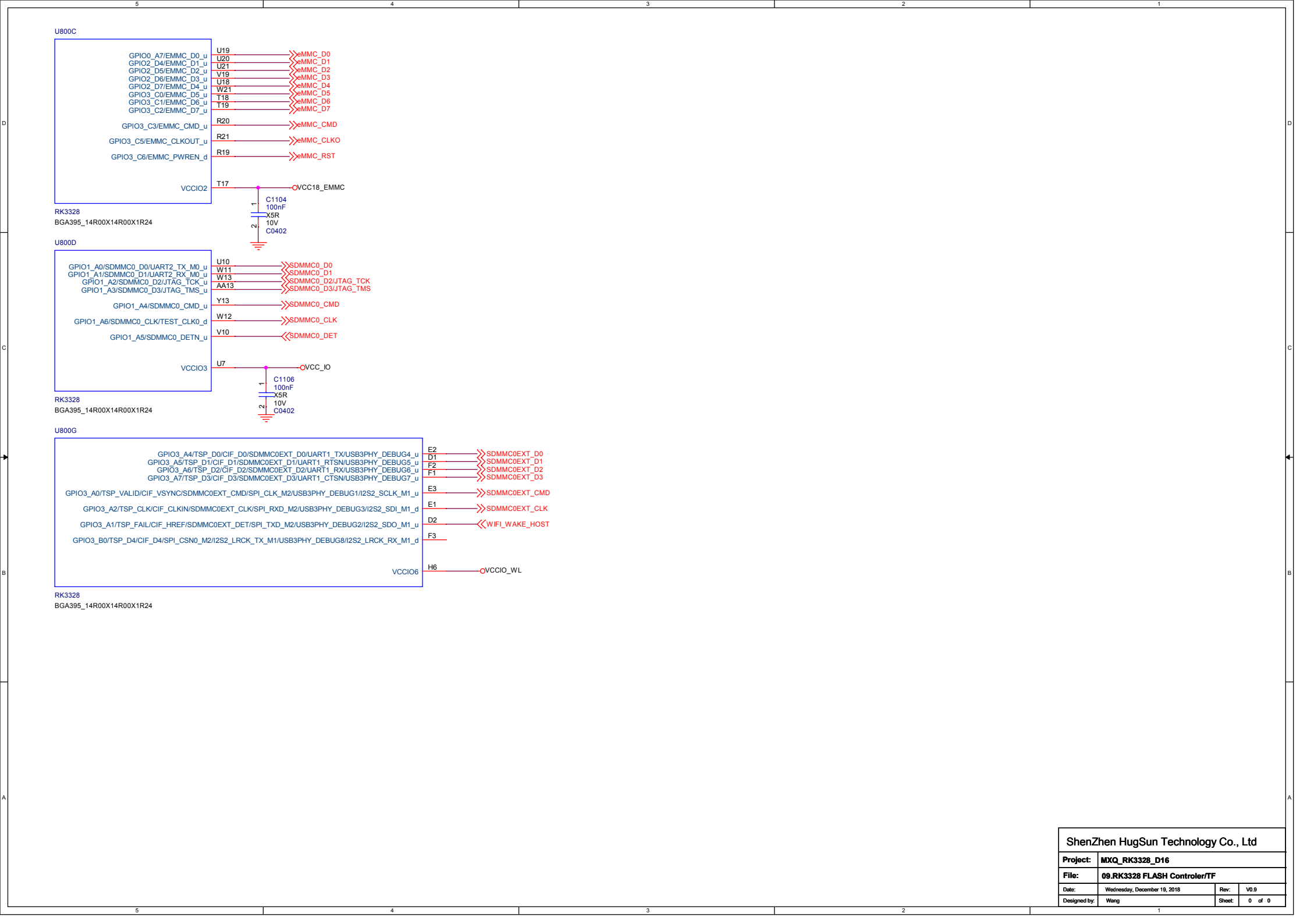


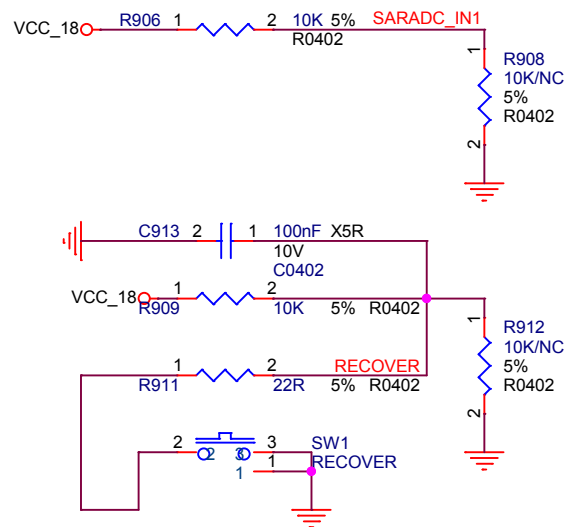
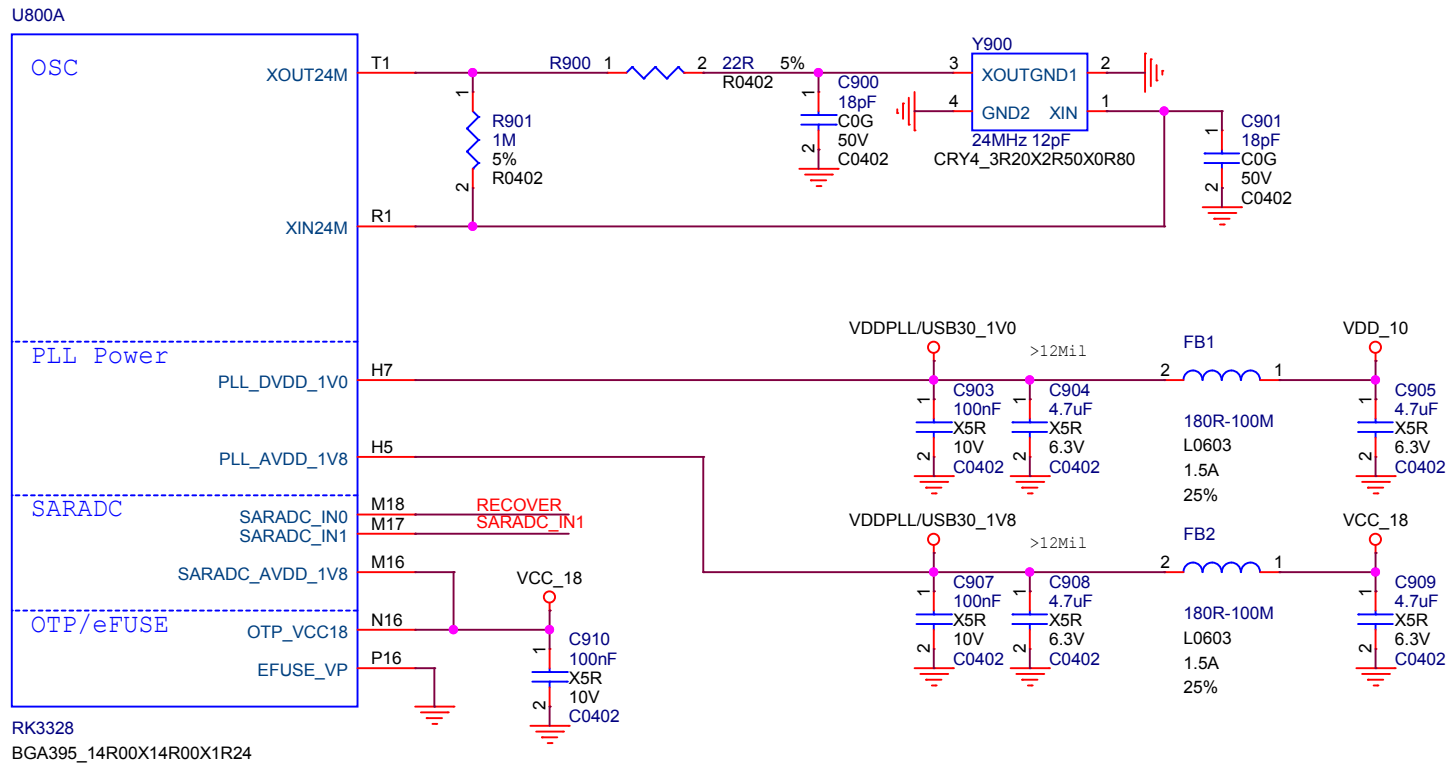
ShenZhen HugSun Technology Co., Ltd

Project:	MXQ_RK3328_D16		
File:	11.RK3328 SDIO/UART/I2C/I2S/IR		
Date:	Wednesday, December 19, 2016	Rev:	V0.9
Designed by:	Wang	Sheet:	0 of 0



ShenZhen HugSun Technology Co., Ltd			
Project:	MXQ_RK3328_D16		
File:	10.RK3328 USB2 PHY/USB3 PHY		
Date:	Wednesday, December 19, 2018	Rev:	V0.9
Designed by:	Wang	Sheet:	0 of 0





SARADC_IN0 BOM版本	SARADC_IN1 PCB版本	Up阻值	Down阻值	IN电压值
Recover		DNP	10K	0V
BOM1		110K	10K	0.15V
BOM2		100K	20K	0.3V
BOM3		100K	33K	0.4466V
BOM4		36K	18K	0.6V
BOM5		51K	36K	0.745V
BOM6		51K	51K	0.9V
BOM7		36K	51K	1.055V
BOM8		18K	36K	1.2V
BOM9		33K	100K	1.353V
BOM10		20K	100K	1.5V
BOM11		10K	110K	1.65V
BOM12		10K	DNP	1.8V

ShenZhen HugSun Technology Co., Ltd			
Project:	MXQ_RK3328_D16		
File:	07.RK3328 OSC/PLL/OTP/SARADC		
Date:	Wednesday, December 19, 2018	Rev:	V0.9
Designed by:	Wang	Sheet:	0 of 0

U800N

ARM

VDD_CORE
VDD_CORE
VDD_CORE
VDD_CORE
VDD_CORE
VDD_CORE
VDD_CORE

GPU/Logic

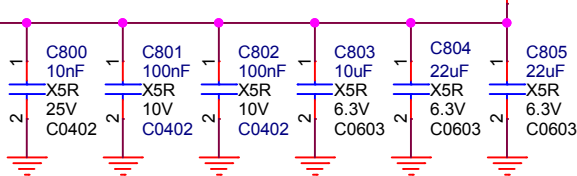
VDD_LOGIC
VDD_LOGIC
VDD_LOGIC
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RK3328

BGA395_14R00X14R00X1R24

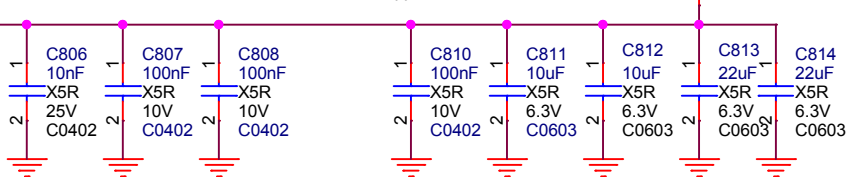
VDD_ARM

>100Mil



VDD_LOG

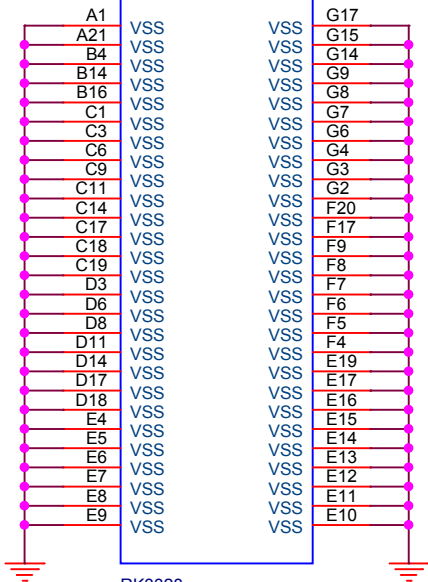
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U800O

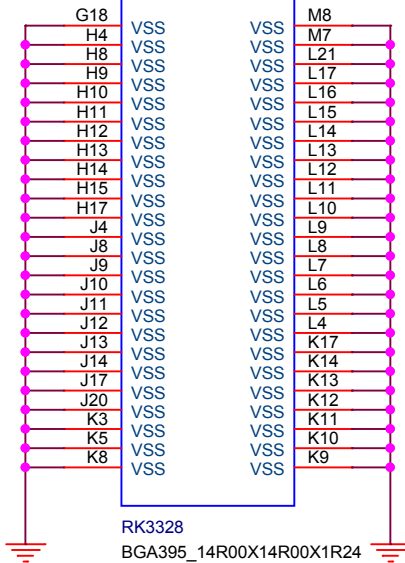
U800P

U800Q



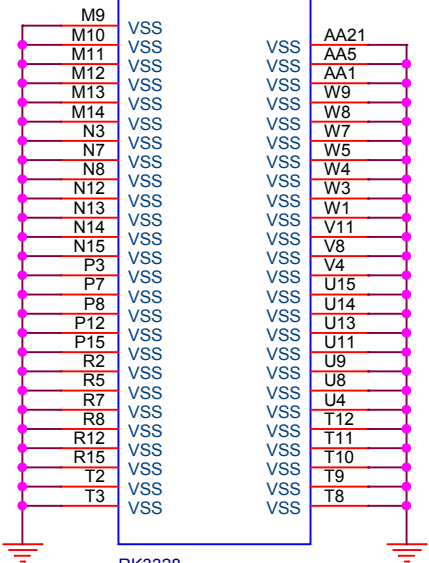
RK3328

BGA395_14R00X14R00X1R24



RK3328

BGA395_14R00X14R00X1R24



RK3328

BGA395_14R00X14R00X1R24

ShenZhen HugSun Technology Co., Ltd

Project: MXQ_RK3328_D16

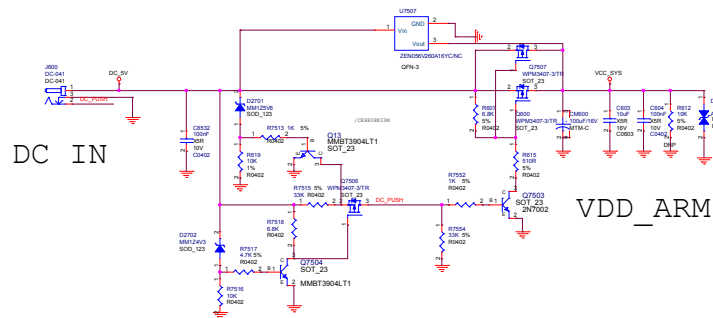
File: 06.RK3328 Power

Date: Wednesday, December 19, 2018

Rev: V0.9

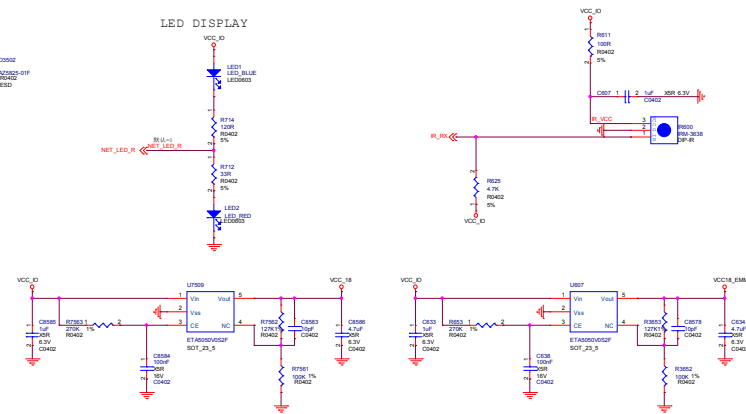
Designed by: Wang

Sheet: 0 of 0

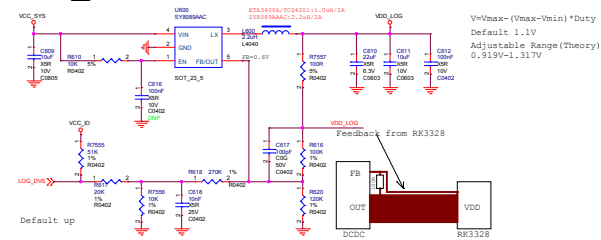


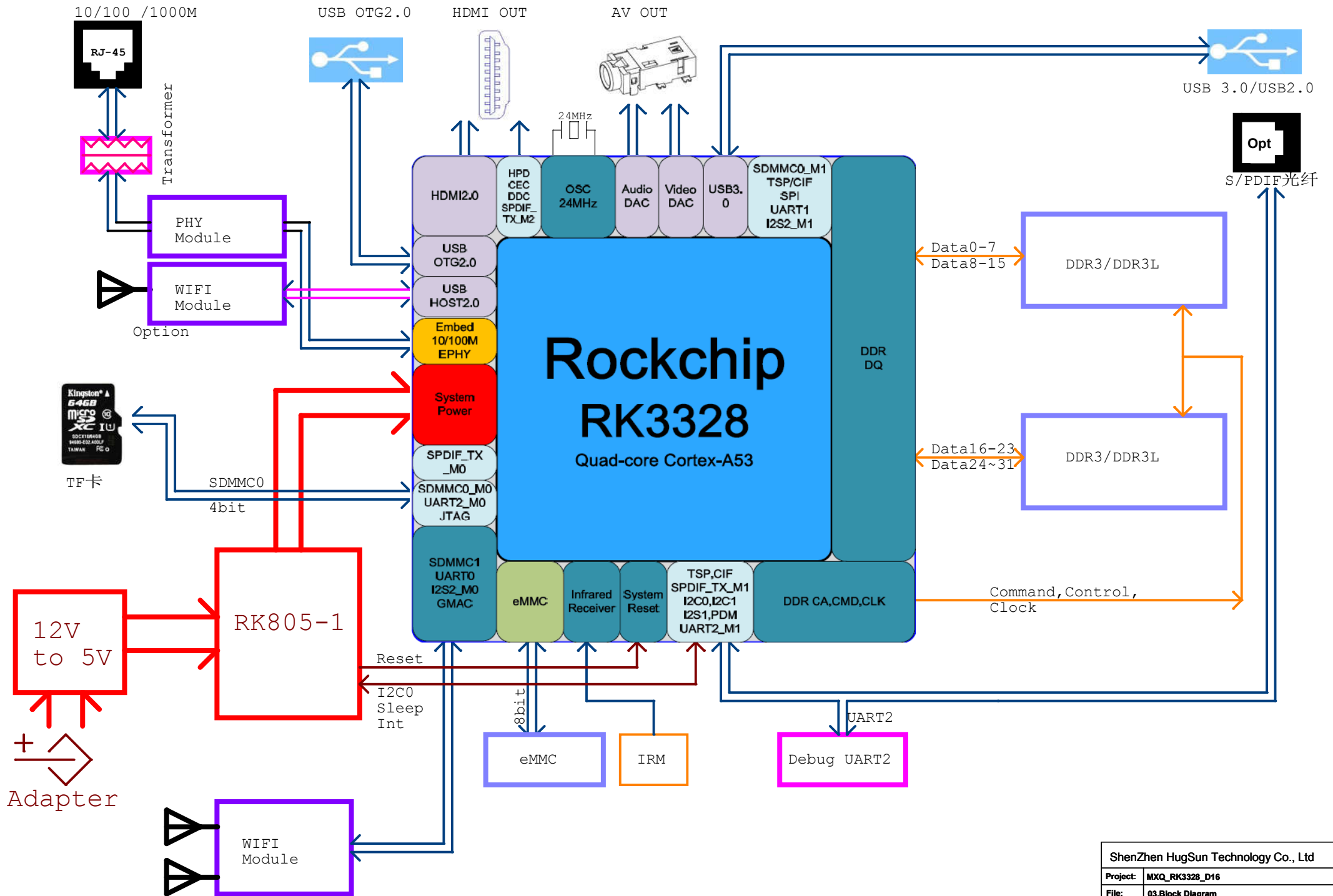
IR Receiver

LED DISPLAY



VDD_LOG





5					4					3					2					1									
Version					Date					Author					Change Note										Approved				
V1.0					20161102					ZDZ,FB					First edictor														

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26.10/100/1000M(Ext PHY)	
27.Debug UART2	

RK3328

Bill of Materials

Header:

Item\tPart\tDescription\tPCB Footprint\tReference\tQuantity\tOption

Combined property string:

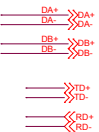
{Item}\t{Value}\t{Description}\t{PCB Footprint}\t{Reference}\t{Quantity}\t{Option}

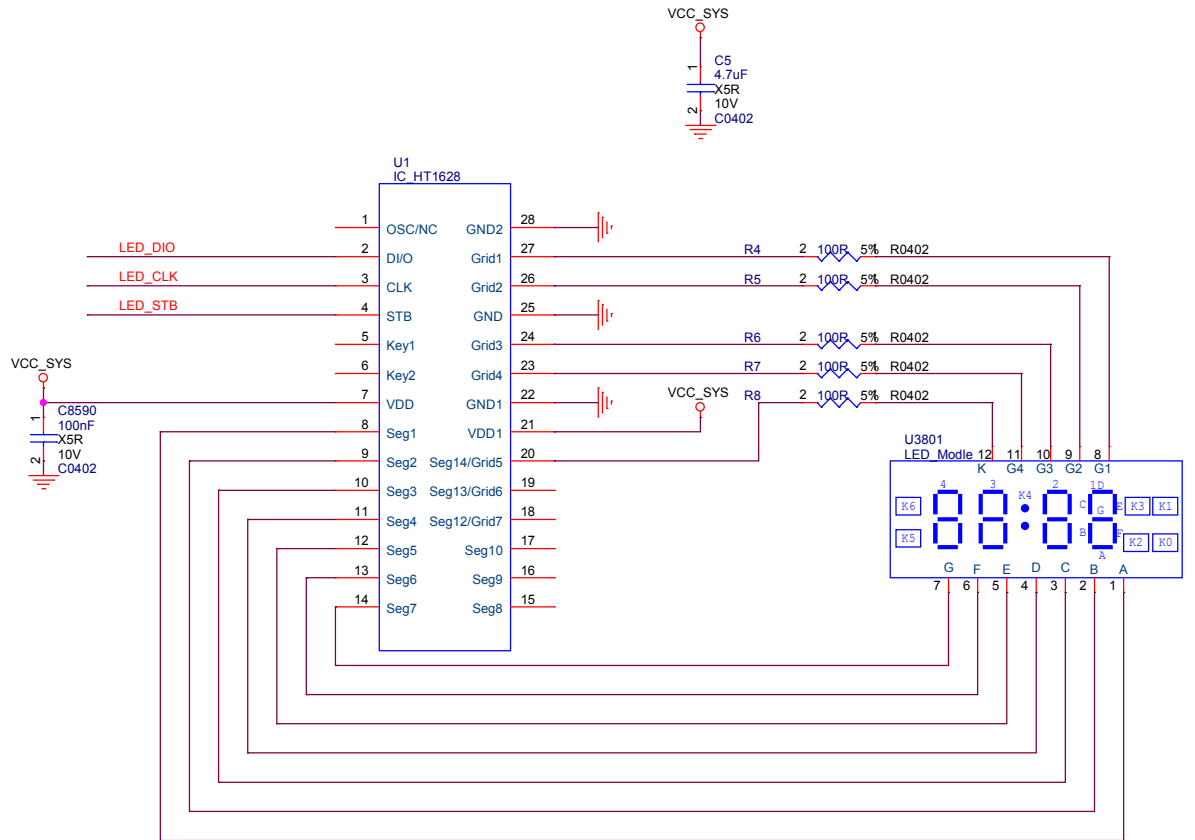
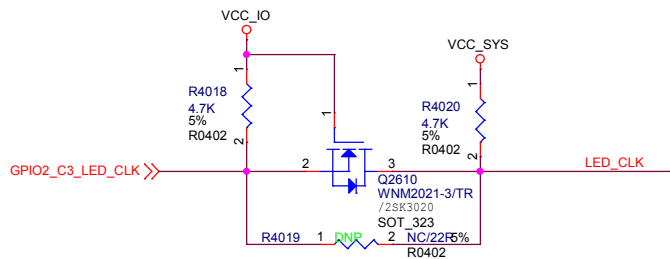
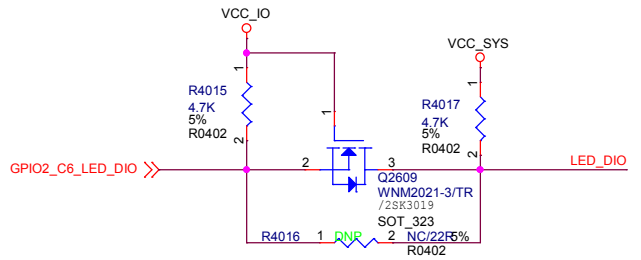
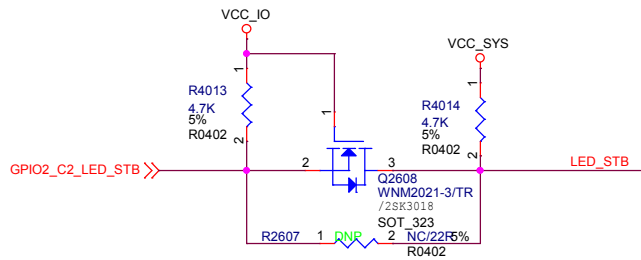
Note:

Component parameter description

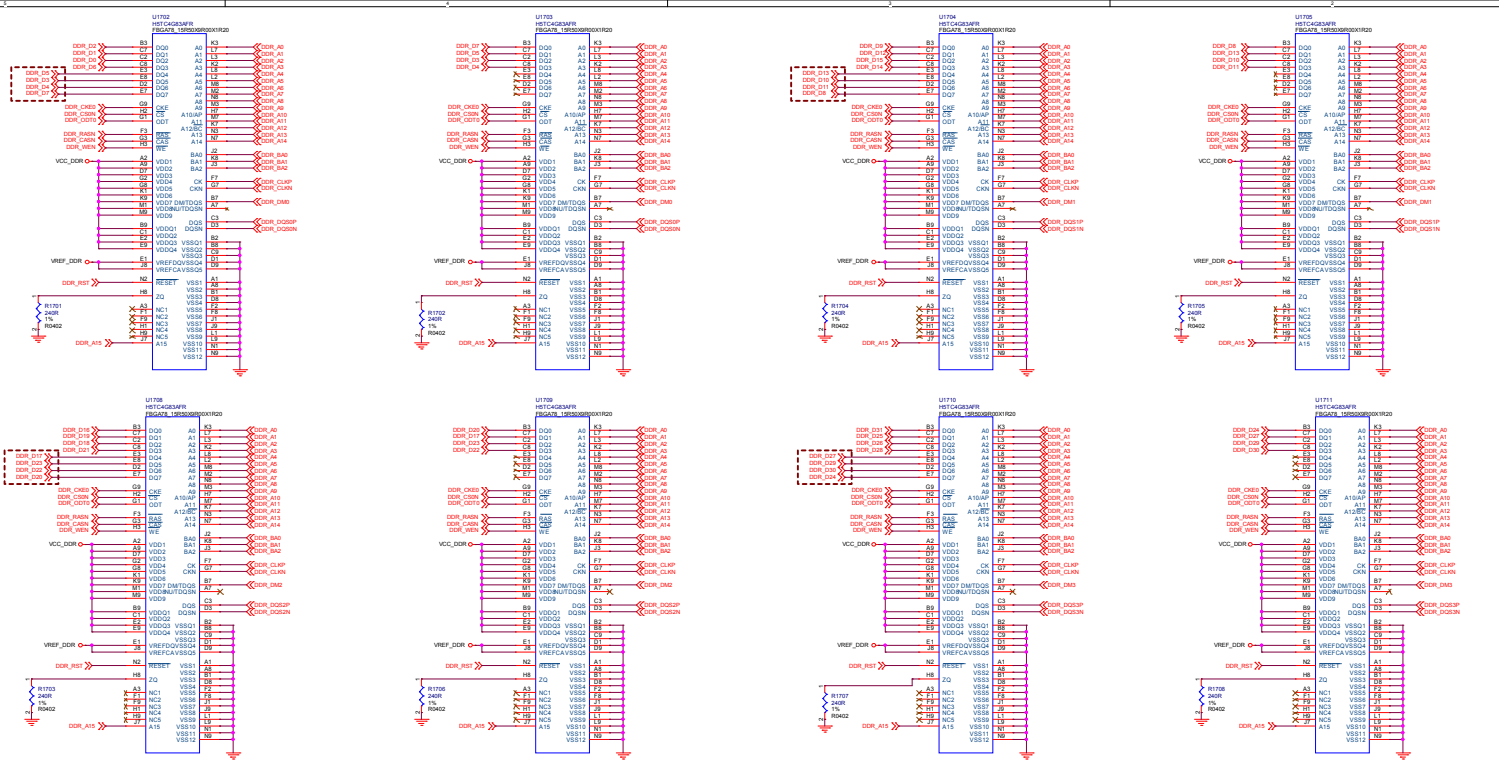
1. DNP stands for component not mounted temporarily

2. If Value or option is DNP, which means the area is reserved without being mounted





Title			<Title>		
Size	Document Number				Rev
B	<Doc>				<RevCode>
Date:	Wednesday, December 19, 2018			Sheet	1 of 1



8X4bit DDR3

